



MYS-ZU5EV 快速使用指南

产品资料链接：

down.myir-tech.com/FZ5

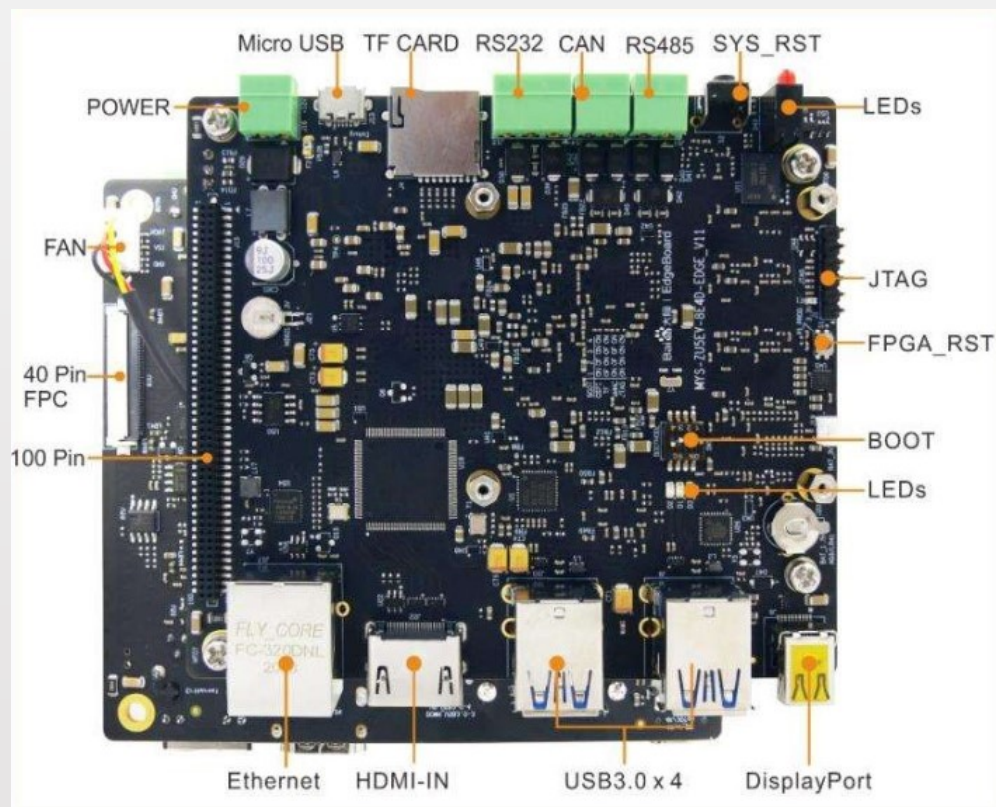
技术支持：

如您遇到使用问题，欢迎与我们联系。

- 邮箱：support.cn@myirtech.com
- 电话：0755-22316235（深圳）
027-59621647/ 027-59621648（武汉）
- 网址：www.myir-tech.com

1.硬件接口及注意事项

MYS-ZU5EV开发板接口图：



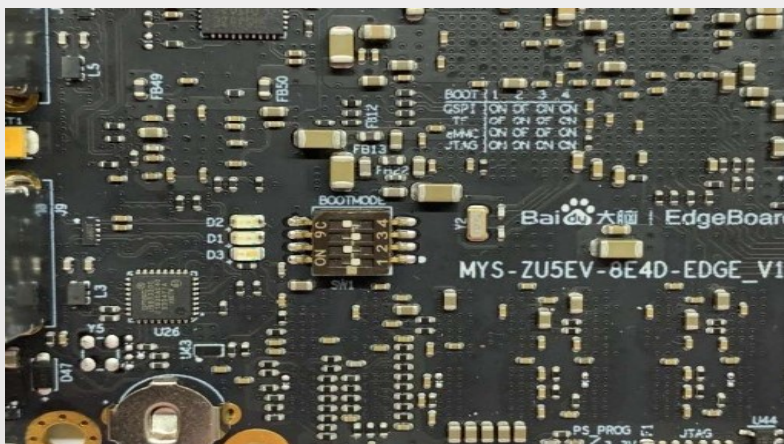
注意事项：

- 1.板卡出厂时已经烧入了出厂镜像程序在TF卡中，您只需要按下面步骤就可以开机启动。
2. 使用12V/2A电源，以避免损坏板卡。

2.快速操作指引

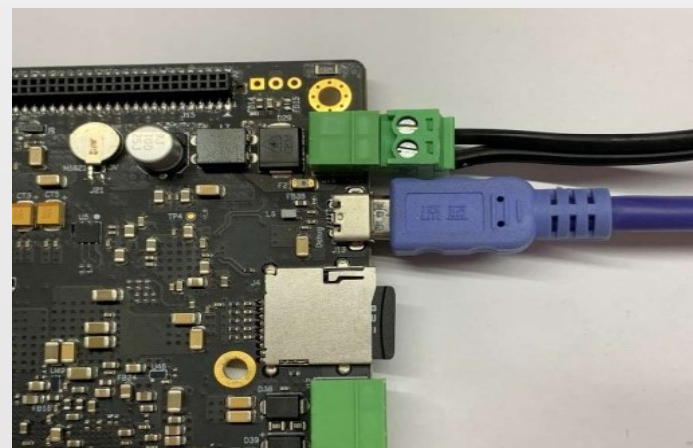
• 第一步：设置BOOT选择

- 设置 SW1 拨码开关，将引导模式设为 TF 启动模式（1010）。（注意对应的 ON=0，OFF=1）



• 第二步：连接调试串口线

- 插好启动用的 TF 卡和 MicroUSB 线，连接电源线。



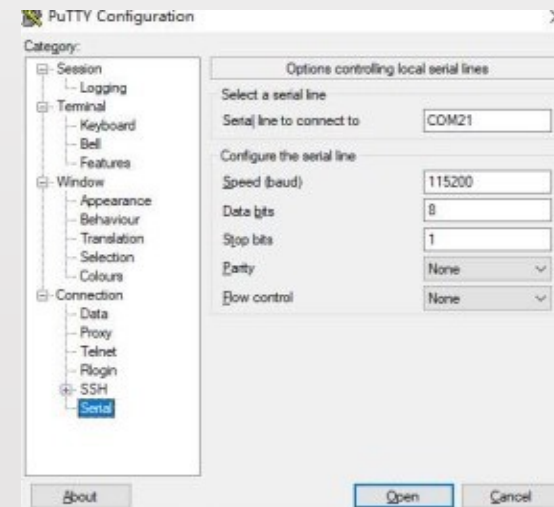
• 第三步：安装调试串口驱动

- 在 PC 上安装好 USB 串口驱动程序，芯片型号为 CP2102N，下载地址：
<https://www.silabs.com/products/development-tools/software/usb-to-uart-bridge-vcp-drivers>

• 第四步：打开调试串口，配置参数，开始调试

- 在主机打开串口终端软件，配置如下参数：

波特率	数据位	停止位	极性	其他
115200	8位	1位	无	无流控



• 第五步：启动板子

- 接上电源，上电启动后终端上将会打印相关启动信息。启动完成后，AI 模式输入用户名 root，密码为 root（测试模式下密码为空），我们将可以通过终端对开发板进行控制。AI 模式下可接好 DP 线，执行 startx 命令启动图形界面进行操作。详细资料信息等可以参考我们的网站，以及附带的光盘资料等。

```
COM21 - PuTTY
[ 5.681128] cfg80211: Loaded X.509 cert 'sforshee: 00b28ddf47aef9cea7'
[ 5.693255] ALSA device list:
[ 5.696214] #0: DisplayPort monitor
[ 5.700111] platform regulatory.0: Direct firmware load for regulatory.db failed with error -2
[ 5.708719] cfg80211: failed to load regulatory.db
[ 5.713588] RAMDISK: gzip image found at block 0
[ 5.836700] usb 1-1: New USB device found, idVendor=05e3, idProduct=0610, bcdDevice=60.60
[ 5.844888] usb 1-1: New USB device strings: Mfr=0, Product=1, SerialNumber=0
[ 5.852025] usb 1-1: Product: USB2.0 Hub
[ 5.904386] hub 1-1:1.0: USB hub found
[ 5.908422] hub 1-1:1.0: 4 ports detected
[ 6.000805] EXT4-fs (ram0): mounted filesystem without journal. Opts: (null)
[ 6.007882] VFS: Mounted root (ext4 filesystem) on device 1:0.
[ 6.013789] devtmpfs: mounted
[ 6.016916] Freeing unused kernel memory: 832K
[ 6.035490] Run /sbin/init as init process
[ 6.046635] EXT4-fs (ram0): re-mounted. Opts: block_validity,delalloc,barrier,user_xattr,acl
Starting logging: OK
Starting mdev...
[ 6.256059] FAT-fs (mmcblk1p1): Volume was not properly unmounted. Some data may be corrupt. Please run fsck.
Initializing random number generator... [ 6.299789] random: dd: uninitialized urandom read (512 bytes read)
done.
Starting network: OK

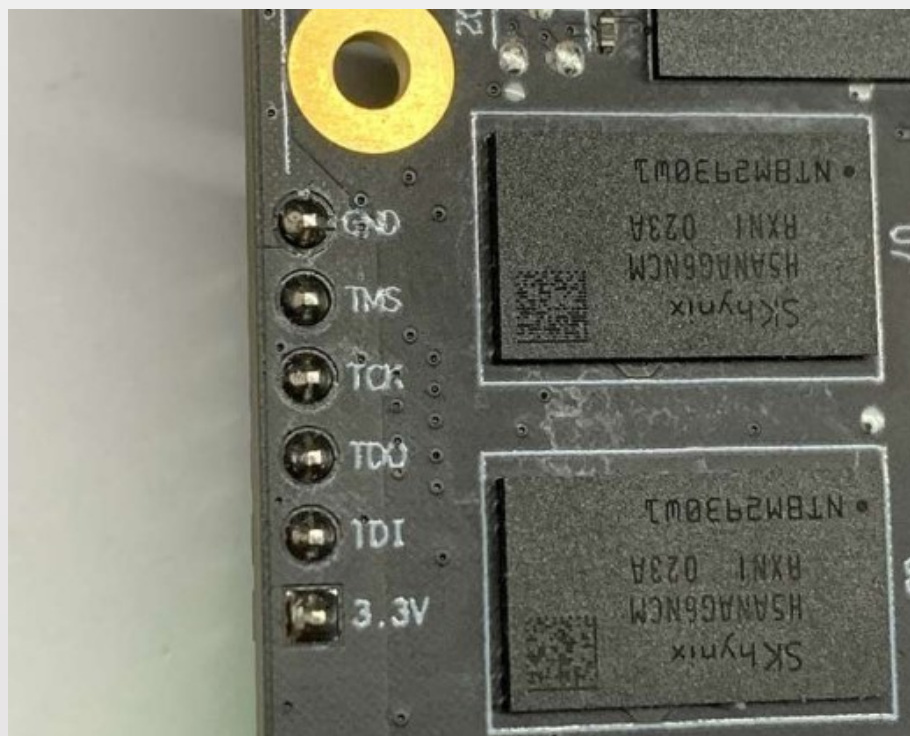
Welcome to myir board
myir login: [ 6.507407] [drm] Cannot find any crtc or sizes

Welcome to myir board
myir login:
Welcome to myir board
myir login: root
[root@myir ~]#
```



• 第六步：板子与仿真器连接

- 开发板与仿真器（下载器）连接如图所示，在位于 J13 的 PCB 背面有标示相关管脚对应的 JTAG 信号定义。





MYS-ZU5EV Development Kit

Quick Start Guide

Software and Documents Be Accessed :
d.myrirtech.com/FZ5

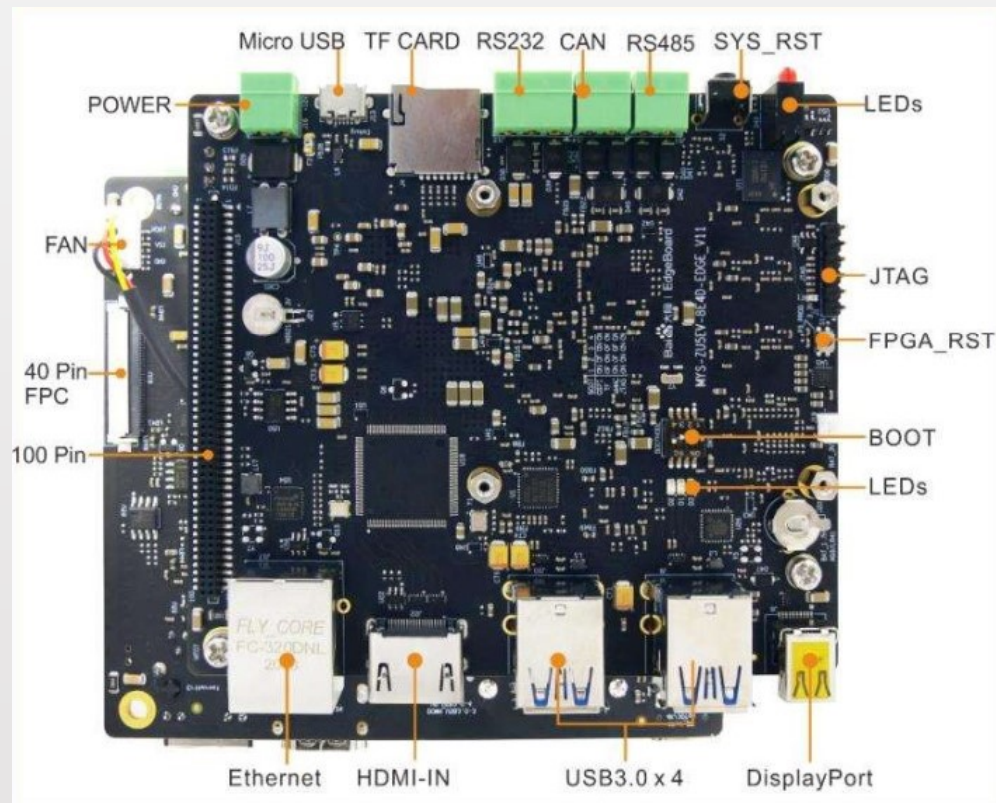
Technical Support :

Please do not hesitate to contact us for technical support:

- Email : support@myirtech.com
- Telephone : +86-755-22984836
- Website : www.myrirtech.com

1. Hardware Interface & Points of Attention

View of MYS-ZU5EV Development Kit



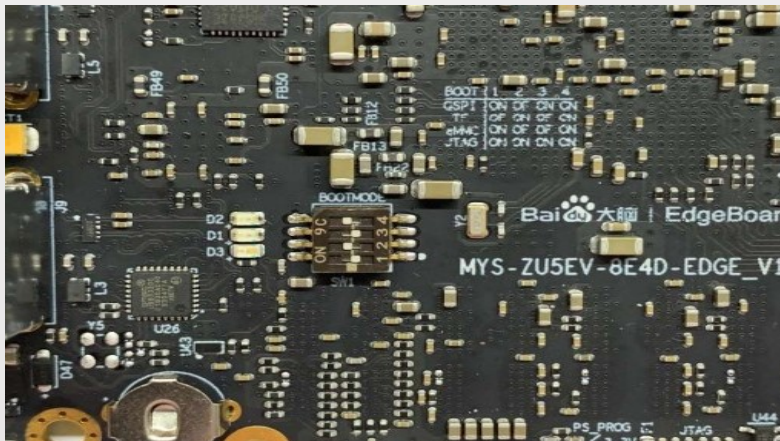
Points of attentions :

1. The factory image of the board card has been burned into the TF card, you only need to follow the following steps to boot and start.
2. To avoid damaging the board ,please make sure using the power adapter(12V/2A DC)

2. Quick operation guide

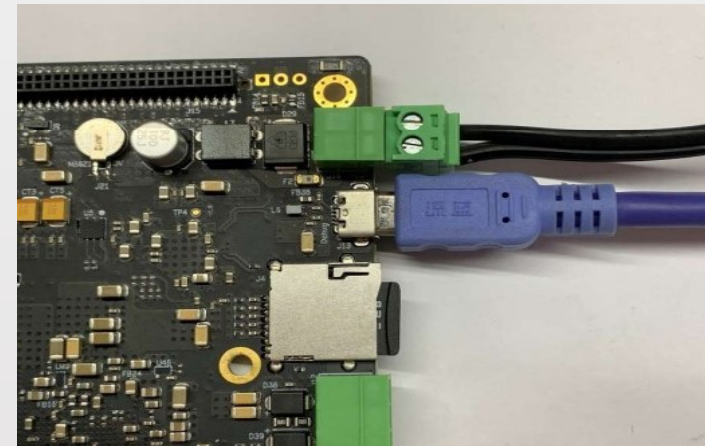
- **Step 1: Set the BOOT selection**

- Set the SW1 dial switch to set the boot mode to TF start mode (1010). (Note the corresponding ON-0, OFF-1)



- **Step 2: Connect the debug serial cable**

- Plug in the TF card and MicroUSB cable for startup, connect the power cable.



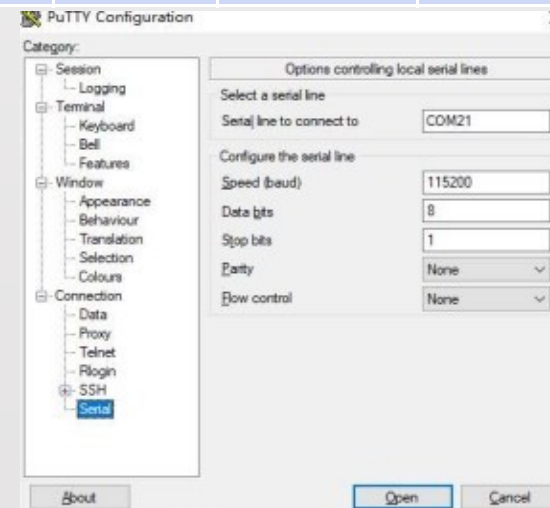
- **Step 3: Install the debug serial driver**

- Install a USB serial driver on your PC with a chip model number CP2102N, download address for driver :
<https://www.silabs.com/products/development-tools/software/usb-to-uart-bridge-vcp-drivers>

- **Step 4: Open the debug serial port, configure the parameters, start debugging**

- Open the serial terminal software on the PC and configure the following parameters :

Band rate	Date bits	Stop bit	Parity	Other
115200	8	1	none	No Flow control



- **Step 5: Start the board**

- Plug in the power, power up after the terminal will print the relevant start-up information. Once started, AI mode enters the user name root and the password is root (the password is empty in test mode), and we will be able to control the board through the terminal. The DP line can be connected in AI mode and the startx command can be executed to start the graphical interface. Details, etc. can be found on our website, as well as the accompanying CD-ROM information.

```
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Welcome to myir board
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```



- **Step 6: The board is connected to the emulator**

- The board is connected to the emulator as shown, and the JTAG signal definition for the relevant pin is indicated on the bottom of the PCB located in J13.

