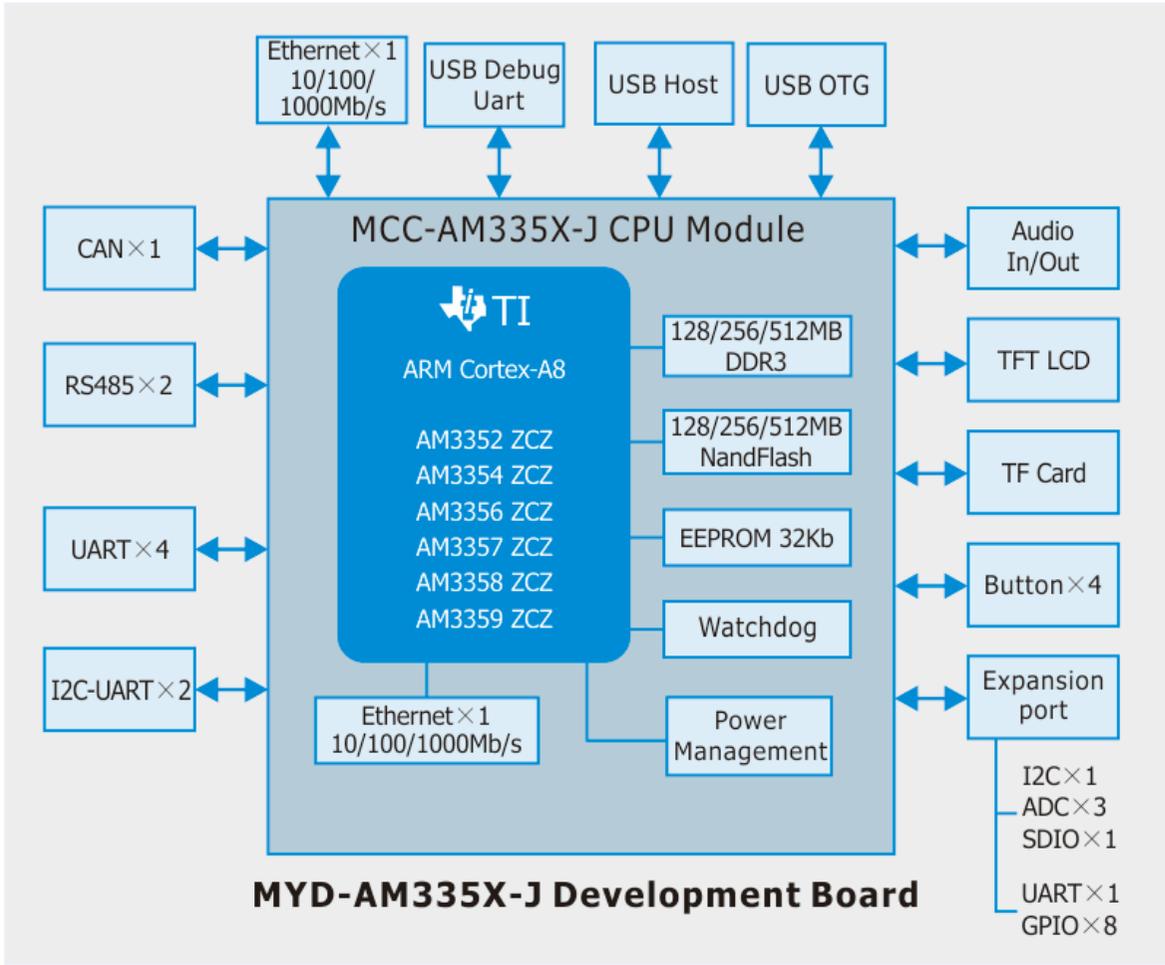
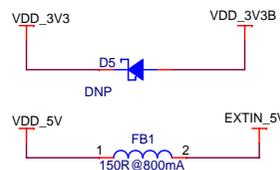
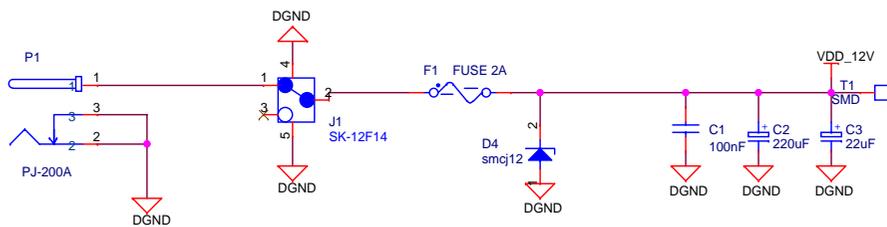
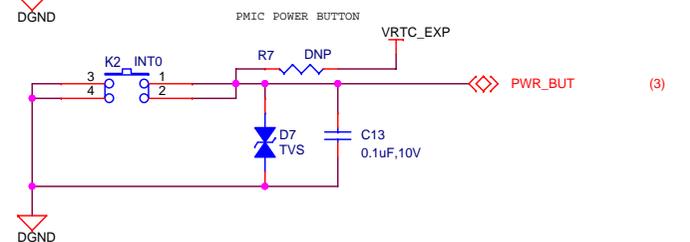
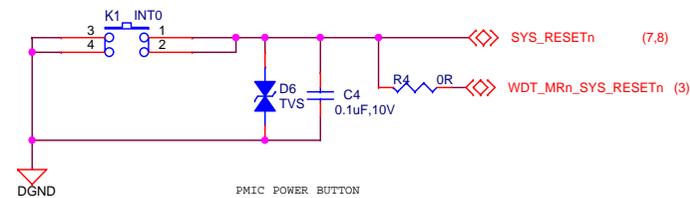
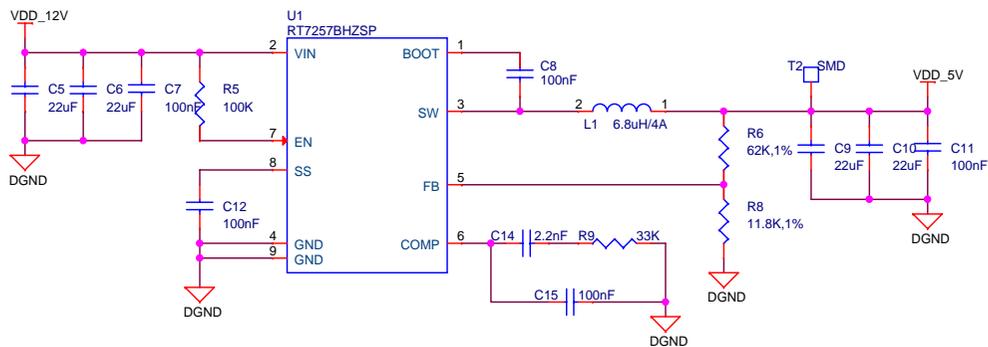
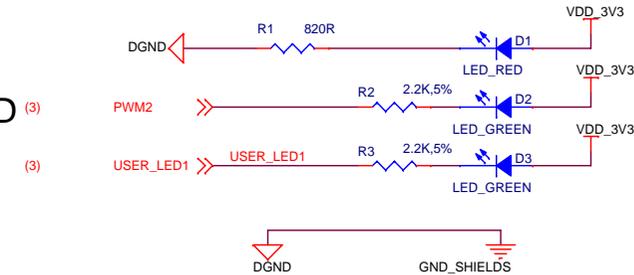


PAGE NO.	SCHEMATIC PAGE
1	TITLE PAGE
2	POWER
3	Exp Conn
4	USB OTG, HOST
5	NET1
6	PHY_NET2
7	LCD
8	Audio
9	UART
10	COM DEBUG
11	RS485 X 1 & CAN X 1
12	MISC

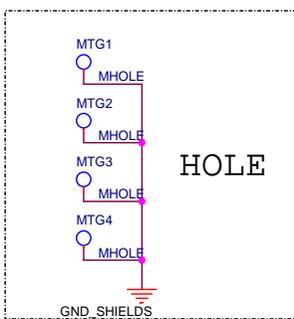
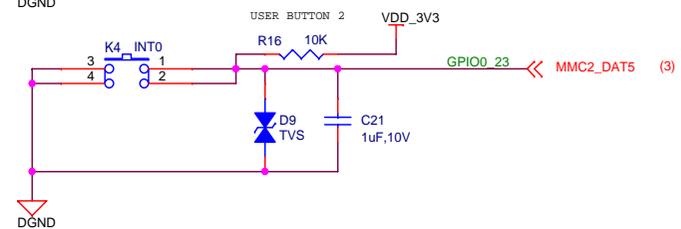
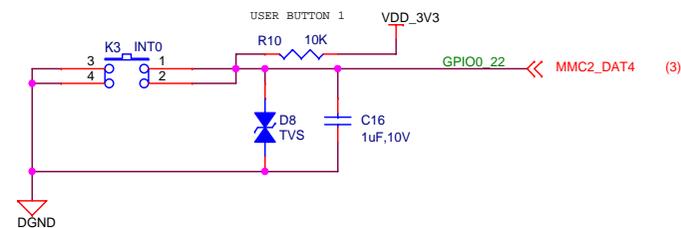
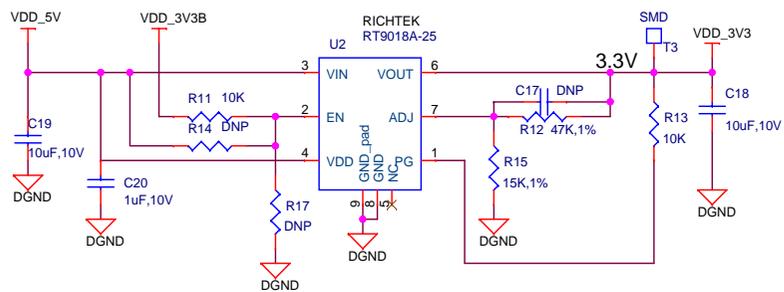




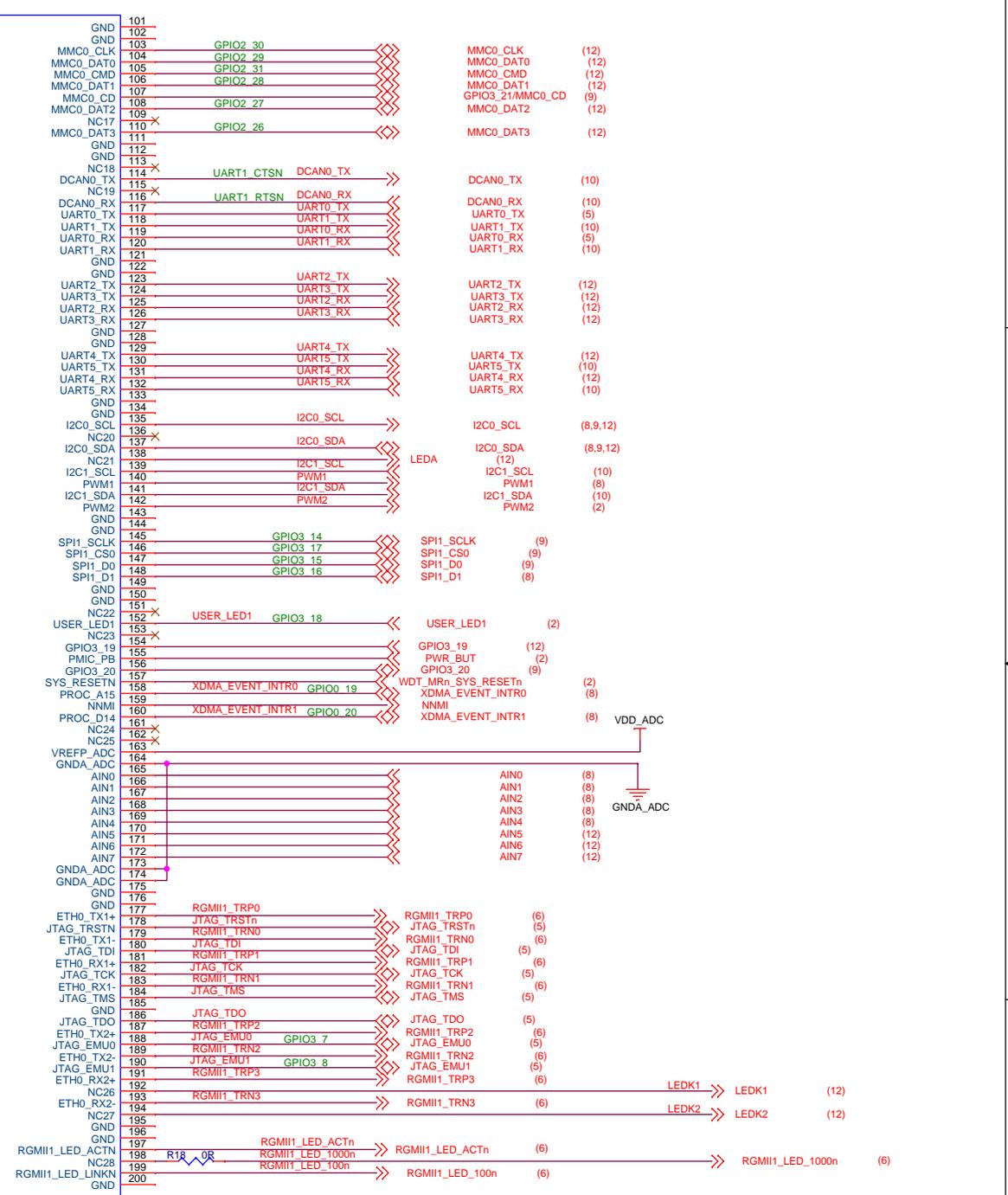
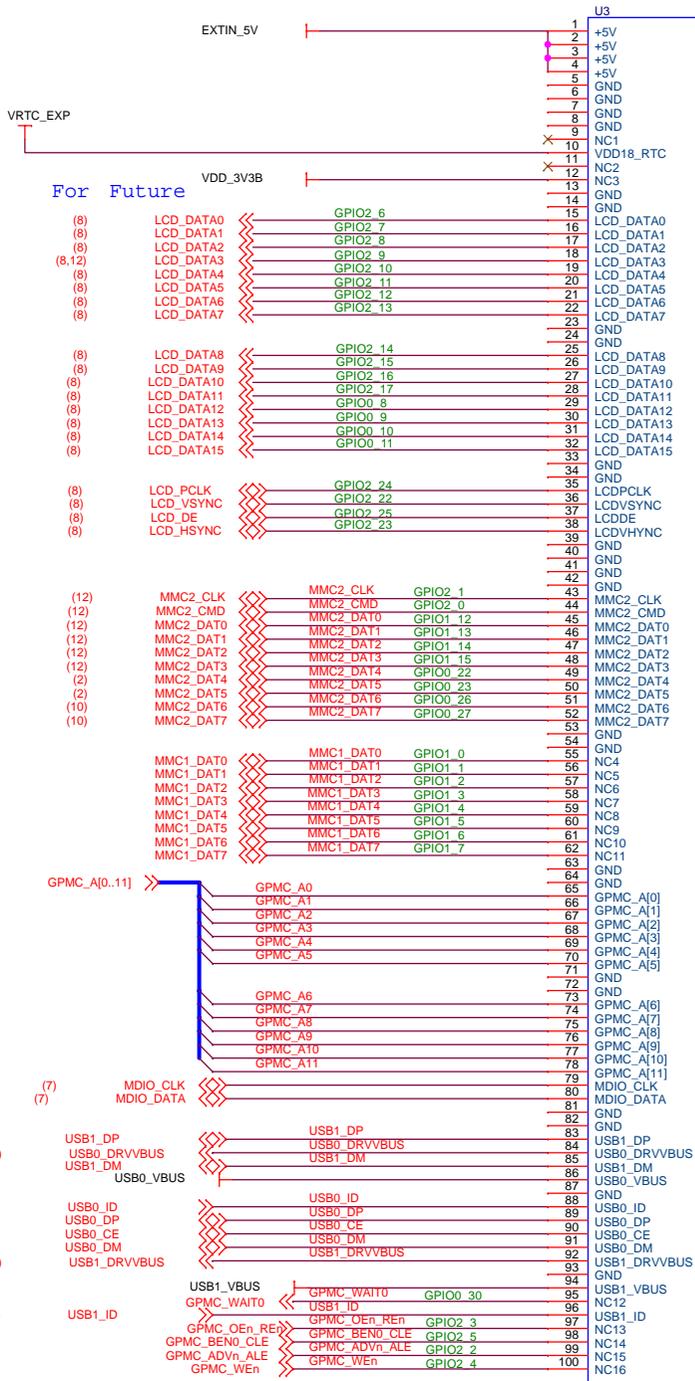
LED (3)



Single PIN Head For TEST  
示波器加地，放在边上空闲 开阔区域



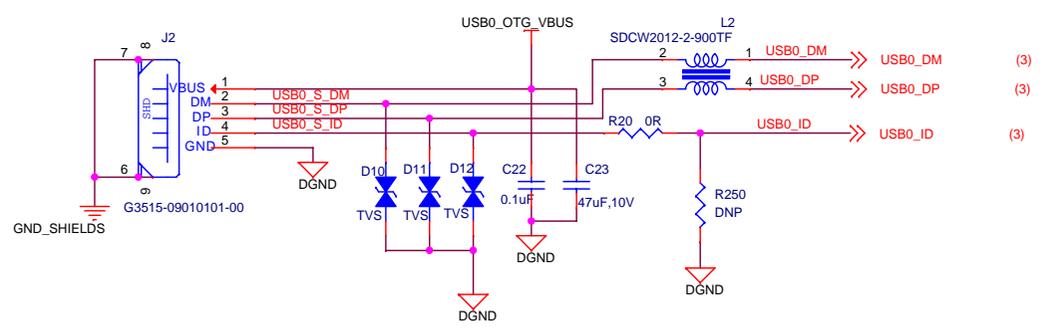
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Size: B	Document Number: Power	Rev: VER 1	
Draw By: MYIR	Date: Tuesday, October 28, 2014	Sheet: 2 of 12	



**MYIR** <http://www.myir-tech.com>

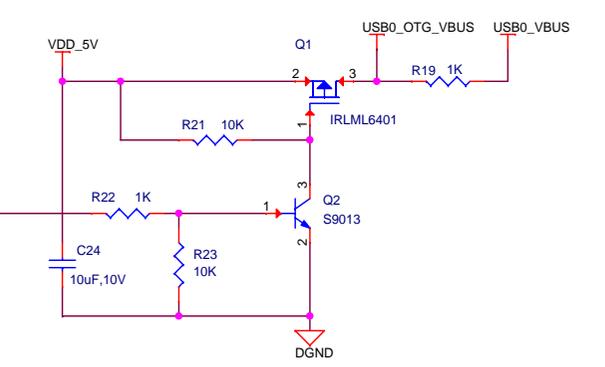
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Size: A3	Document Number: EXP CONN	Rev: VER 1
Draw By: MYIR	Date: Tuesday, October 28, 2014	Sheet: 3 of 12

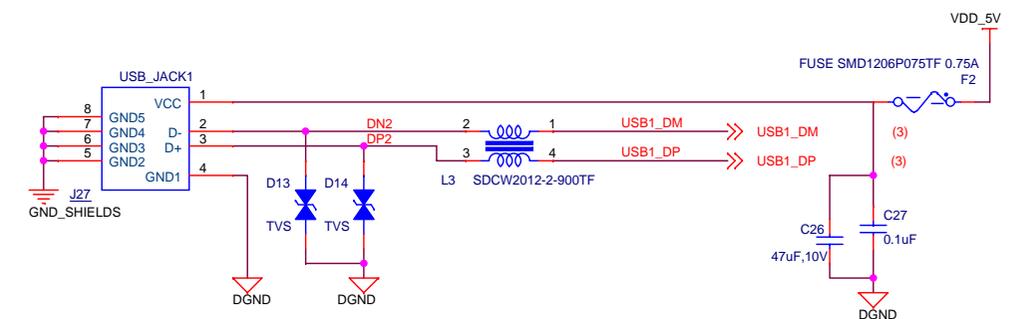
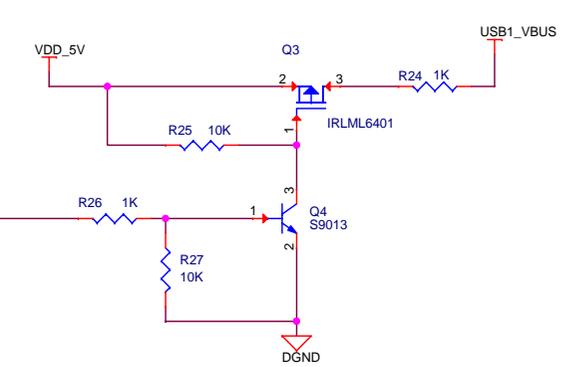


(3) USB0\_DM  
(3) USB0\_DP  
(3) USB0\_ID

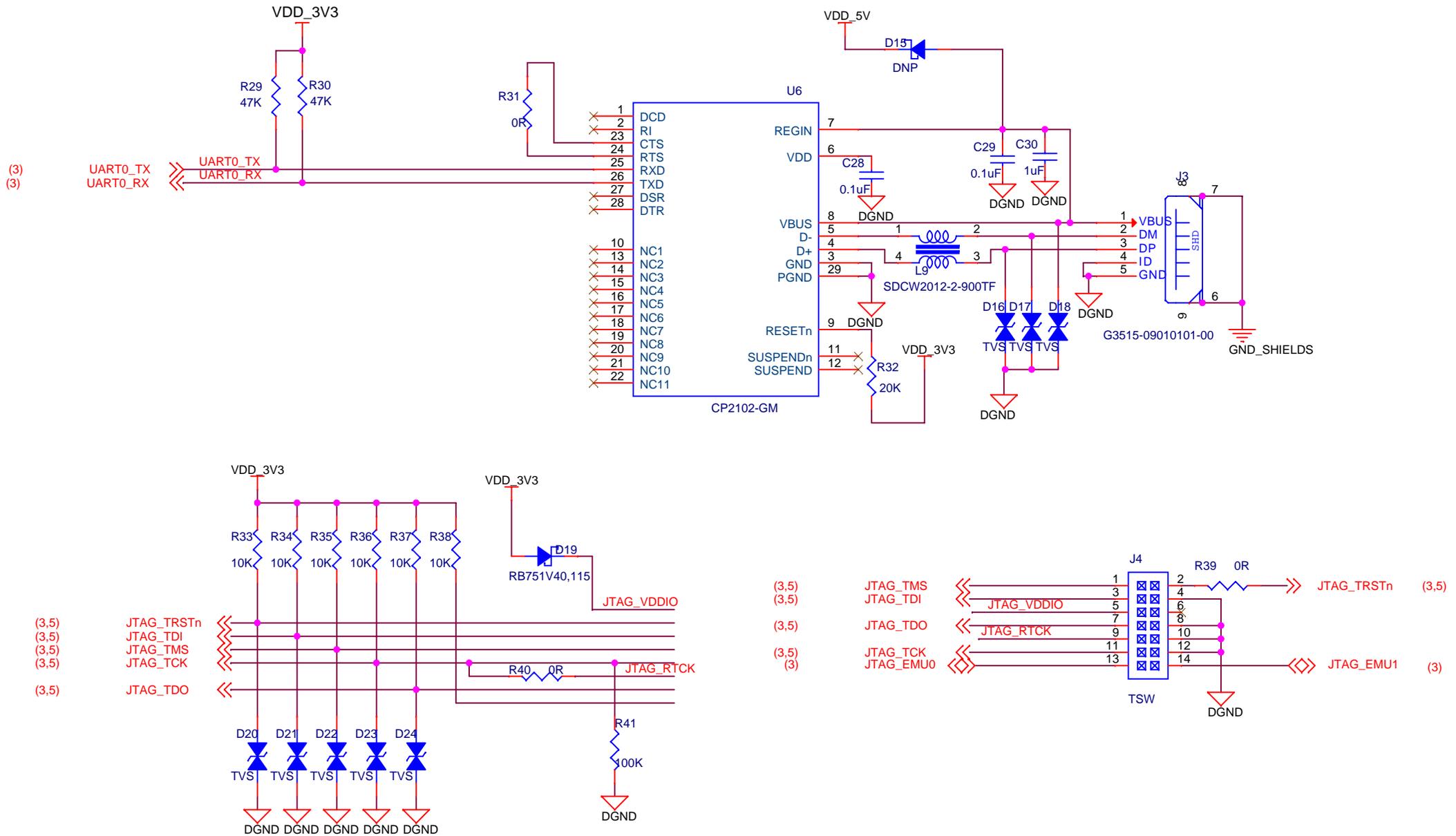
(3) USB0\_DRVVBUS



(3) USB1\_ID  
(3) USB1\_DRVVBUS



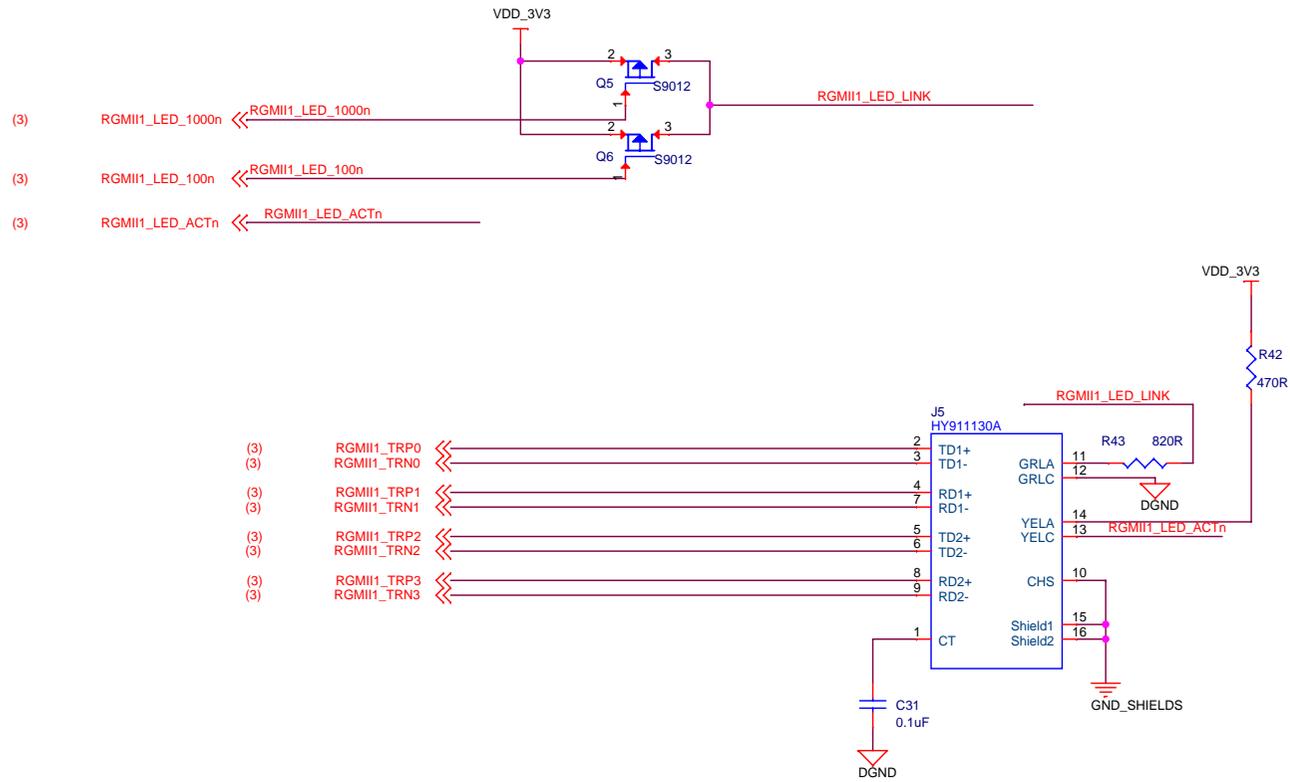
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Size: B	Document Number: USB	Rev: VER 1	
Draw By: MYIR	Date: Tuesday, October 28, 2014	Sheet: 4 of 12	

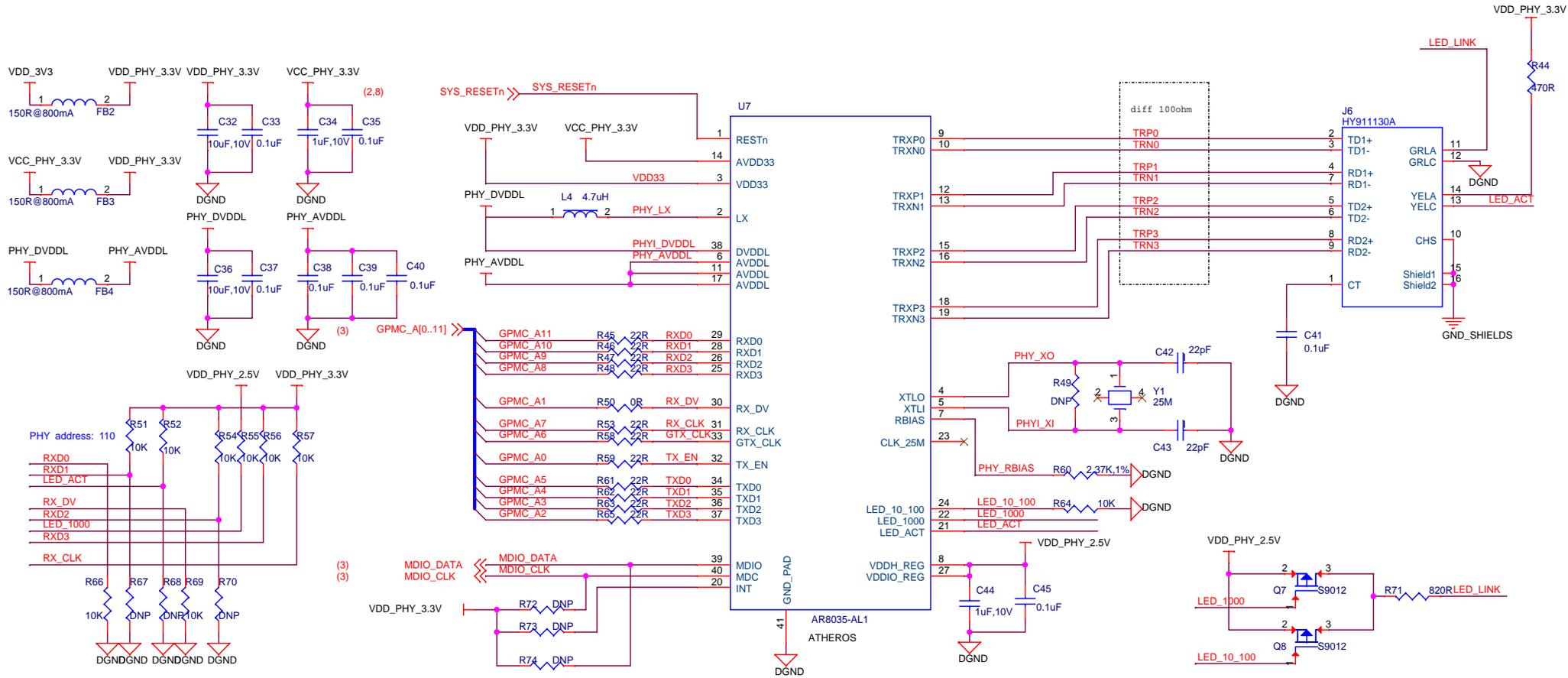


(3,5) JTAG\_TRSTn  
 (3,5) JTAG\_TDI  
 (3,5) JTAG\_TMS  
 (3,5) JTAG\_TCK  
 (3,5) JTAG\_TDO

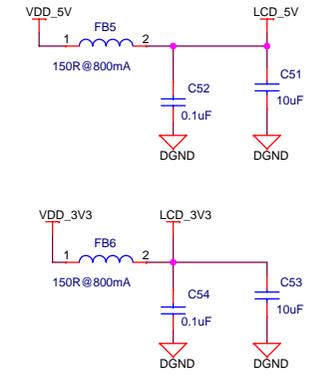
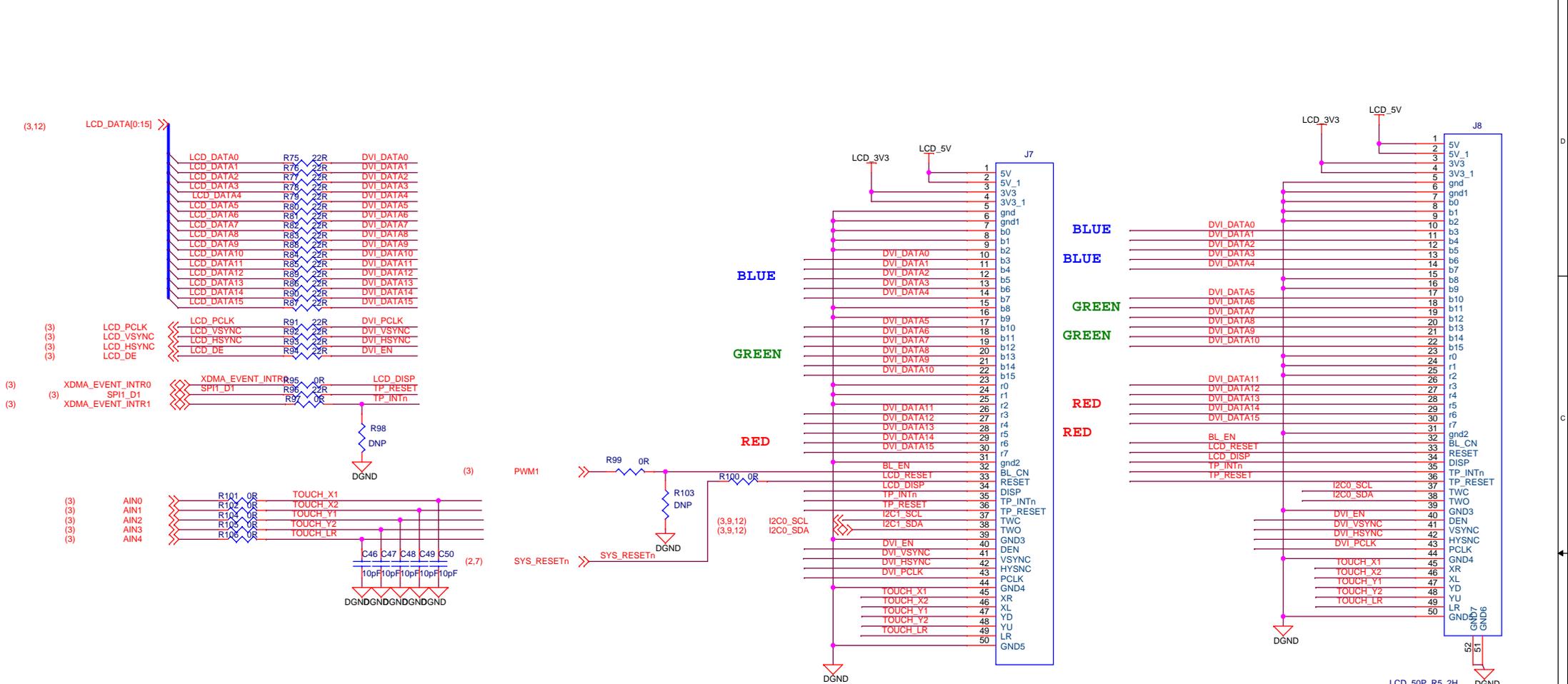
(3,5) JTAG\_TMS  
 (3,5) JTAG\_TDI  
 (3,5) JTAG\_TDO  
 (3,5) JTAG\_TCK  
 (3) JTAG\_EMU0  
 (3) JTAG\_EMU1

		<a href="http://www.myr-tech.com">http://www.myr-tech.com</a>	
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		<a href="http://www.myir-tech.com">http://www.myir-tech.com</a>	
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Size: B	Document Number: PHY_NET2	Rev: VER 1	
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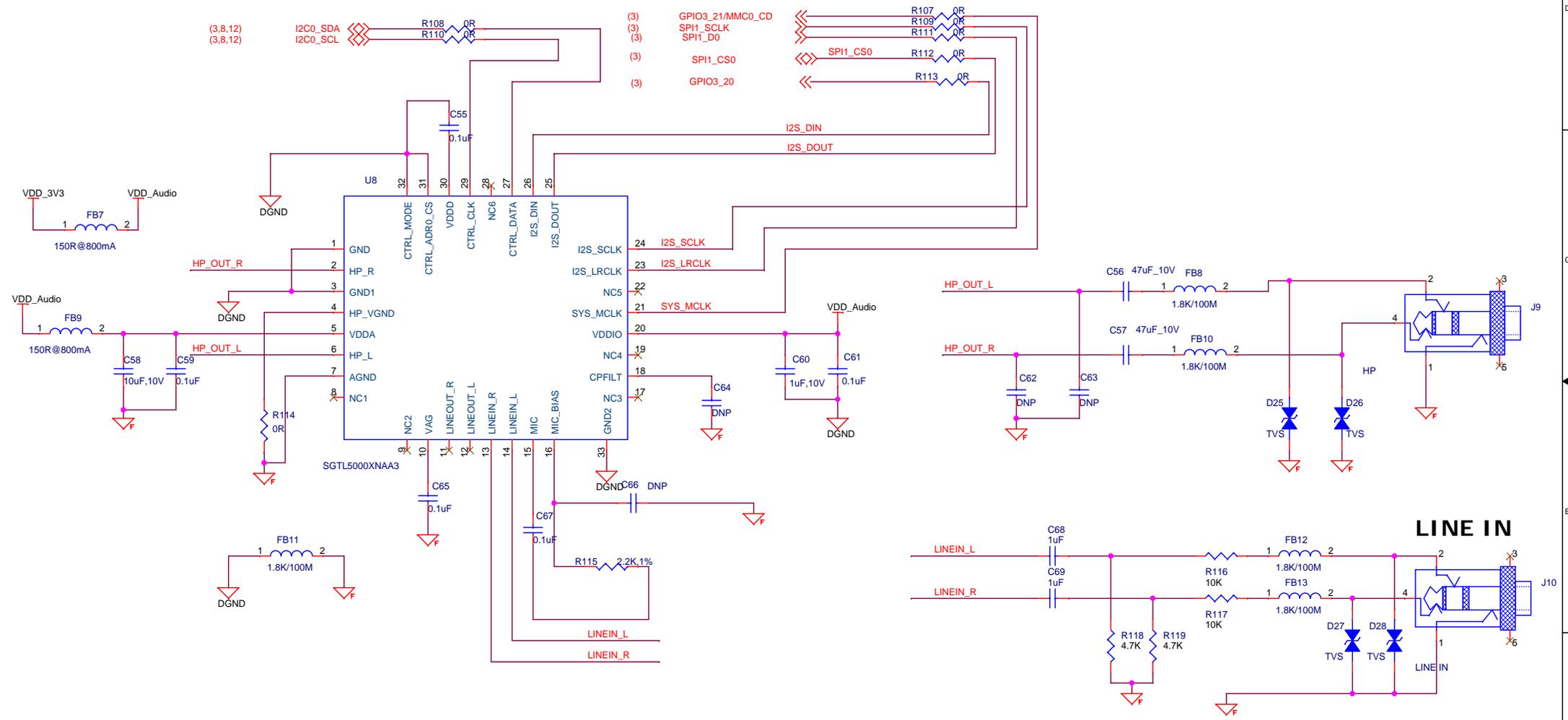


(3,8,12) I2C0\_SDA  
 (3,8,12) I2C0\_SCL

(3) GPIO3\_21/MMC0\_CD  
 (3) SPI1\_SCLK  
 (3) SPI1\_D0

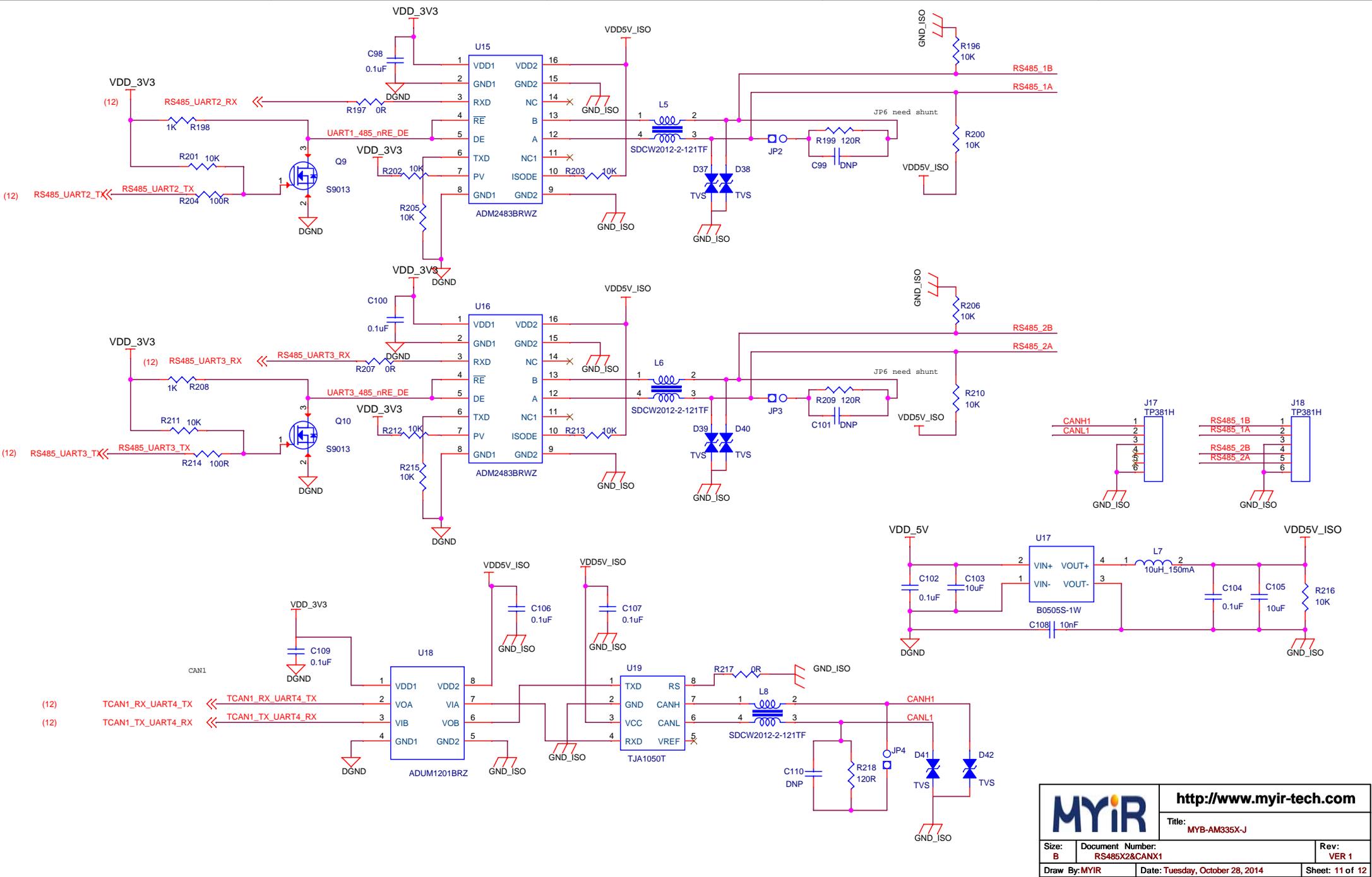
(3) SPI1\_CS0

(3) GPIO3\_20

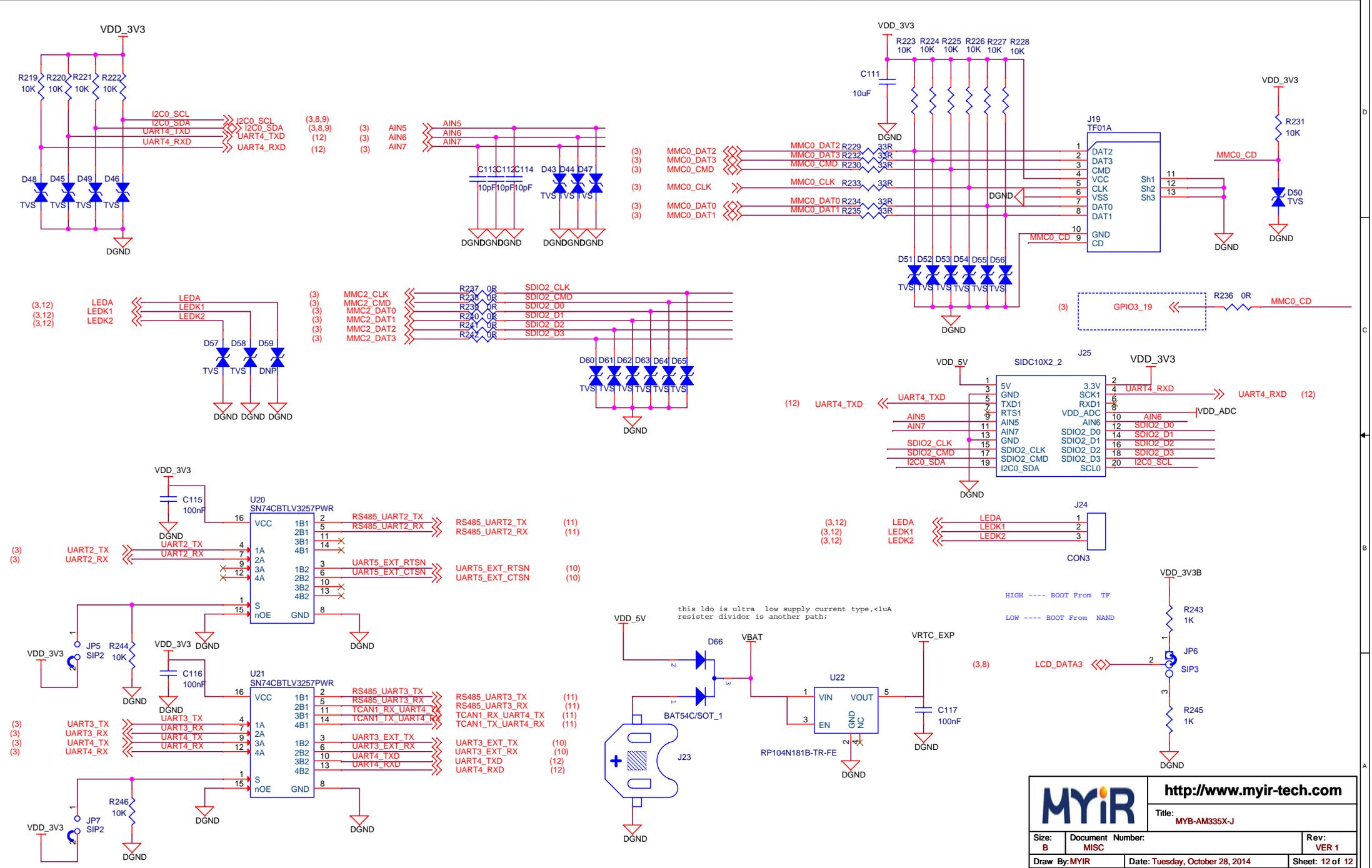


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		<a href="http://www.myir-tech.com">http://www.myir-tech.com</a>	
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Size: B	Document Number: RS485X2&CANX1	Rev: VER 1	
Draw By: MYIR	Date: Tuesday, October 28, 2014	Sheet: 11 of 12	



		<a href="http://www.myir-tech.com">http://www.myir-tech.com</a>	
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