



Ref	Num	Sch Name	BGA	Type	Description	Pin Mux									
						Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7	Mode 8	Mode 9
	53	GPI04_7	AC24	I	Camera1 Data9	cam1_data9		dss_data16	pr0_pru0_gpo17	spi2_cs3	pr0_pru0_gpi17	EMU9	gpio4_7	uart0_ctsn	
	54	GPI04_6	AA19	I	Camera0 Data9	cam0_data9		dss_data17	pr0_pru0_gpo16	spi2_cs3	pr0_pru0_gpi16	EMU8	gpio4_6		
	55	GPI04_5	AB19	I	Camera0 Data8	cam0_data8		dss_data18	pr0_pru0_gpo15	spi2_cs2	pr0_pru0_gpi15	EMU7	gpio4_5	I2C2_SCL	
	56	GPI04_4	AC20	I	CCD Data Pixel Clock	cam0_pclk		dss_data19	pr0_pru0_gpo14	spi2_cs0	pr0_pru0_gpi14	EMU6	gpio4_4	I2C2_SDA	
	57	GPI04_3	AD17	I	CCD Data Write Enable	cam0_wen		dss_data20	cam0_data11	spi2_d0	cam1_data11	EMU5	gpio4_3		
	58	GPI04_2	AC18	IO	CCD Data Field Indicator	cam0_field		dss_data21	cam0_data10	spi2_sclk	cam1_data10	EMU4	gpio4_2		
	59	GPI04_1	AD18	IO	CCD Data Vertical Detect	cam0_vd		dss_data22	pr1_edio_outvalid	spi2_d1	EMU11	EMU3	gpio4_1		
	60	GPI04_0	AE17	IO	CCD Data Horizontal Detect	cam0_hd		dss_data23	pr1_edio_sof	spi2_cs1	EMU10	EMU2	gpio4_0		
	61	CAM0_DATA0	AE18	I	Camera0 Data0	cam0_data0		cam1_data9	I2C1_SDA	pr0_pru1_gpo16	pr0_pru1_gpi16	ehrpwm0_synco	gpio5_19		
	62	CAM0_DATA1	AB18	I	Camera0 Data1	cam0_data1		cam1_data8	I2C1_SCL	pr0_pru1_gpo17	pr0_pru1_gpi17	ehrpwm3_synco	gpio5_20		
	63	CAM0_DATA2	Y18	I	Camera0 Data2	cam0_data2	mmc1_clk	cam1_data10	qspi_clk				gpio4_24		
	64	CAM0_DATA3	AA18	I	Camera0 Data3	cam0_data3	mmc1_cmd	cam1_data11	qspi_csn				gpio4_25		
	65	CAM0_DATA4	AE19	I	Camera0 Data4	cam0_data4	mmc1_dat0	cam1_wen	qspi_d0			ehrpwm3A	gpio4_26		
	66	CAM0_DATA5	AD19	I	Camera0 Data5	cam0_data5	mmc1_dat1		qspi_d1			ehrpwm3B	gpio4_27		
	67	CAM0_DATA6	AE20	I	Camera0 Data6	cam0_data6	mmc1_dat2		qspi_d2			ehrpwm1A	gpio4_28		
	68	CAM0_DATA7	AD20	I	Camera0 Data7	cam0_data7	mmc1_dat3		qspi_d3			ehrpwm1B	gpio4_29		
	69	MDIO_CLK	B17	0	MDIO Clock	mdio_clk	timer5	uart5_txd	uart3_rtsn	mmc0_sdpw	mmc1_clk	mmc2_clk	gpio0_1	pr1_mdio_mdclk	
	70	GND													
	71	GPI05_13	E24	IO	SW4 Gpio						pr1_mii1_rxlink		gpio5_13		
	72	MDIO_DATA	A17	IO	MDIO Data	mdio_data	timer6	uart5_rxd	uart3_ctsn	mmc0_sdc_d	mmc1_cmd	mmc2_cmd	gpio0_0	pr1_mdio_data	
	73	GPI01_28	A3	IO	MMC/SD/SDIO Data line 3	gpmc_be1n	gmii2_col	gpmc_csn6	mmc2_dat3	gpmc_dir	pr1_mii1_col	mcasp0_aclkr	gpio1_28		
	74	UART4_RXD	A2	I	UART4 Receive Data	gpmc_wait0	gmii2_crs	gpmc_csn4	rmii2_crs_dv	mmc1_sdc_d	pr1_mii1_crs	uart4_rxd	gpio0_30		gpio5_30
	75	RGMII2_TCTL	C3	0	RGMII2 Transmit Control	gpmc_a0	gmii2_txen	rgmii2_tctl	rmii2_txen	gpmc_a16	pr1_mii1_txen	ehrpwm1_tripzone_input	gpio1_16		
	76	UART4_TXD	B3	0	UART4 Transmit Data	gpmc_wpn	gmii2_rxer	gpmc_csn5	rmii2_rxer	mmc2_sdc_d	pr1_mii1_rxer	uart4_txd	gpio0_31		gpio5_31
	77	RGMII2_TCLK	E8	0	RGMII2 Transmit Clock	gpmc_a6	gmii2_txclk	rgmii2_tclk	mmc2_dat4	gpmc_a22	pr1_mii1_mtl_clk	eQEP1_index	gpio1_22		
	78	RGMII2_RCTL	C5	IO	MMC/SD/SDIO Data line 0	gpmc_a1	gmii2_r								

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						Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7	Mode 8	Mode 9
	5	GND													
	6	GND													
	7	V3_3D													
	8	GND													
	9	V3_3D													
	10	V1_8D													
	11	GND													
	12	VRTC													
	13	PMIC_PB		I	Push-button Monitor Input										
	14	GND													
	15	RESET_IN		I	System Manual Reset										
	16	SYS_RESETh		0	System Reset										
	17	LCD_BLEN	G24	IO	LCD Backlight Enable	CAP0_in_PWM0_out	uart3_txd	spi1_cs1	r1_ecap0_ecap_capin_apwm	spi1_sclk	mmc0_sdwp	xdma_event_intr2	gpio0_7	ehrpwm2B	timer1
	18	GPIO0_24	H20	IO	Use For DSS Reset	clkreq							gpio0_24		
	19	MCASP0_AHCLKR	M24	IO	McASP0 Receive Master	mcasp0_ahclk	ehrpwm0_synci	mcasp0_axr2	spi1_cs0	eCAP2_in_PWM2_out	pr0_pru0_gpo3	pr0_pru0_gpi3	gpio3_17		
	20	MCASP1_AXR1	L24	IO	McASP0 Transmit Master	mcasp0_ahclkx	eQEP0_strobe	mcasp0_axr3	mcasp1_axr1	EMU4	pr0_pru0_gpo7	pr0_pru0_gpi7	gpio3_21		
	21	MCASP0_AXR0	H23	IO	McASP0 Serial Data	mcasp0_axr0	hrpwm0_tripzone_inp	spi1_cs3	spi1_d1	mmc2_sdc	pr0_pru0_gpo2	pr0_pru0_gpi2	gpio3_16		
	22	MCASP1_AXR0	M25	IO	McASP0 Serial Data	mcasp0_axr1	eQEP0_index	mcasp1_axr0	EMU3	pr0_pru0_gpo6	pr0_pru0_gpi6	gpio3_20	gpio0_2		
	23	MCASP0_FXS	N22	IO	McASP0 Transmit Frame Sync	mcasp0_fxs	ehrpwm0B	spi1_cs2	spi1_d0	mmc1_sdc	pr0_pru0_gpo1	pr0_pru0_gpi1	gpio3_15		
	24	MCASP1_FXS	K23	IO	McASP0 Receive Frame Sync	mcasp0_fsr	eQEP0B_in	mcasp0_axr3	mcasp1_fxs	EMU2	pr0_pru0_gpo5	pr0_pru0_gpi5	gpio3_19		gpio0_19
	25	MCASP0_ACLKX	N24	IO	McASP0 Transmit Bit Clock	mcasp0_aclkx	ehrpwm0A	spi0_cs3	spi1_sclk	mmc0_sdc	pr0_pru0_gpo0	pr0_pru0_gpi0	gpio3_14		
	26	MCASP1_ACLKX	L23	IO	McASP0 Receive Bit Clock	mcasp0_aclkr	eQEP0A_in	mcasp0_axr2	mcasp1_aclkx	mmc0_sdwp	pr0_pru0_gpo4	pr0_pru0_gpi4	gpio3_18		gpio0_18
	27	UART3_RTSh	K24	0	UART3 Request to Send	uart3_rtsn	hdq_sio			pr0_pru1_gpo19	pr0_pru1_gpi19	ehrpwm5B	gpio5_1		
	28	UART3_RXD	H25	IO	UART3 Receive Data	uart3_rxd				pr0_pru0_gpo18	pr0_pru0_gpi18	ehrpwm4A	gpio5_2		
	29	UART3_CTSn	H22	IO	UART3_CTSN	uart3_ctsn		spi4_cs1		pr0_pru1_gpo18	pr0_pru1_gpi18	ehrpwm5A	gpio5_0		
	30	UART3_TXD	H24	IO	UART3_TXD	uart3_txd				pr0_pru0_gpo19	pr0_pru0_gpi19	ehrpwm4B	gpio5_3		
	31	GND													
	32	GND													
	33	SPI2_CS0	T23	IO	Use For LCD Reset	spi2_cs0	I2C1_SDA					ehrpwm2_tripzone_input	gpio3_25		gpio0_23
	34	SPI2_SCLK	N20	IO	Use For HDMI_INTn	spi2_sclk	I2C1_SCL					ehrpwm4_tripzone_input	gpio3_24		gpio0_22
	35	SPI2_D0	P22	IO	Use For TOUCH_INTn	spi2_d0						ehrpwm5_tripzone_input	gpio3_22		gpio0_20
	36	SPI2_D1	P20	IO	Use For TP Reset	spi2_d1						ehrpwm1_tripzone_input	gpio3_23		gpio0_21
	37	SPI0_CS0	T20	IO	SPI0 Chip Select0	spi0_cs0	mmc2_sdwp	I2C1_SCL	ehrpwm0_synci	pr1_uart0_txd	pr0_uart0_txd	pr1_edio_data_out1	gpio0_5	ehrpwm1B	
	38	SPI0_SCLK	P23	IO	SPI0 Clock	spi0_sclk	uart2_rxd	I2C2_SDA	ehrpwm0A	pr1_uart0_cts_n	pr0_uart0_cts_n	EMU2	gpio0_2		
	39	SPI0_D0	T22	IO	SPI0 Data line 0	spi0_d0	uart2_txd	I2C2_SCL	ehrpwm0B	pr1_uart0_rts_n	pr0_uart0_rts_n	EMU3	gpio0_3		
	40	SPI0_D1	T21	IO	SPI0 Data line 1	spi0_d1	mmc1_sdwp	I2C1_SDA	ehrpwm0_tripzone_input	pr1_uart0_rxd	pr0_uart0_rxd	pr1_edio_data_out0	gpio0_4	ehrpwm1A	
	41	USB1_DRVVBUS	F25	0	USB1 Active high VBUS	USB1_DRVVBUS							gpio3_13		gpio0_25
	42	GND													
	43	USB0_DRVVBUS	G21	0	USB0 Active high VBUS	USB0_DRVVBUS							gpio0_18		gpio5_27
	44	USB1_DP	V24	A	USB1 Data plus	USB1_DP									
	45	USB1_VBUS	T25	A	USB1 VBUS	USB1_VBUS									
	46	USB1_DM	V25	A	USB1 Data minus	USB1_DM									
	47	USB0_VBUS	U23	A	USB0 VBUS	USB0_VBUS									
	48	GND													
	49	USB1_ID	U25	A	USB1 ID	USB1_ID									
	50	USB0_DP	W25	A	USB0 Data plus	USB0_DP									
	51	USB0_ID	U24	A	USB0 ID	USB0_ID									
	52	USB0_DM	W24	A	USB0 Data minus	USB0_DM									
	53	SPI4_D1	P24	IO	SPI4 Data line 1	spi4_d1						ehrpwm0_tripzone_input	gpio5_6		
	54	GND													
	55	SPI4_D0	R24	IO	SPI4 Data line 0	spi4_d0						ehrpwm3_synci	gpio5_5		
	56	I2C0_SCL	Y22	IOD		I2C0_SCL	timer7	uart2_rtsn	eCAP1 in PWM1 out				gpio3_6		

