

Ref	Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
				Signal Name	Mode		
J3	1	VDD_5V				5V	5V input
	2	VDD_5V				5V	5V input
	3	VDD_5V				5V	5V input
	4	VDD_5V				5V	5V input
	5	VDD_5V				5V	5V input
	6	VDD_5V				5V	5V input
	7	VDD_5V				5V	5V input
	8	VDD_5V				5V	5V input
	9	DGND				0V	
	10	DGND				0V	
	11	DGND				0V	
	12	DGND				0V	
	13	DGND				0V	
	14	DGND				0V	
	15	DGND				0V	
	16	DGND				0V	
	17	VDD_3V33				3.3V	Built in PMU chip(BD71847MWV) pin Out
	18	VDD_1V8				1.8V	Built in PMU chip(BD71847MWV) pin Out
	19	VDD_3V33				3.3V	Built in PMU chip(BD71847MWV) pin Out
	20	VDD_1V8				1.8V	Built in PMU chip(BD71847MWV) pin Out
	21	VDD_3V33				3.3V	Built in PMU chip(BD71847MWV) pin Out
	22	VDD_1V8				1.8V	Built in PMU chip(BD71847MWV) pin Out
	23	VDD_3V33				3.3V	Built in PMU chip(BD71847MWV) pin Out
	24	VDD_1V8				1.8V	Built in PMU chip(BD71847MWV) pin Out
	25	DGND					
	26	DGND					
27	M_2_32K_OUT	AG14		GPIO1_I000	Default	1.8V	
				GPIO1_I000	ALT0		
				CCMSRCGPCMIX_ENET_PHY_REF_CLK_ROOT	ALT1		
				ANAMIX_REF_CLK_32K	ALT5		
				CCMSRCGPCMIX_EXT_CLK1	ALT6		
28	SD2_DET	AB9		GPIO1_I015	Default	1.8V	
				GPIO1_I015	ALT0		
				USB2_OTG_OC	ALT1		
				USDHC3_WP	ALT4		
				PWM4_OUT	ALT5		
				CCMSRCGPCMIX_CLK02	ALT6		
29	SYS_nRST			# System Reset Button			Built in PMU chip(BD71847MWV) 40pin
30	CLK01	AC9		GPIO1_I014	Default	1.8V	
				GPIO1_I014	ALT0		
				USB2_OTG_PWR	ALT1		
				USDHC3_CD_B	ALT4		
				PWM3_OUT	ALT5		
				CCMSRCGPCMIX_CLK01	ALT6		
31	SAI3_MCLK	AD6		GPIO5_I002	Default	3.3V	
				SAI3_MCLK	ALT0		
				PWM4_OUT	ALT1		
				SAI5_MCLK	ALT4		
				GPIO5_I002	ALT5		
32	LVDS_RSTLV	AD9		GPIO1_I013	Default	1.8V	
				GPIO1_I0013	ALT0		
				USB1_OTG_OC	ALT1		
				PWM2_OUT	ALT5		
33	SAI3_TXFS	AC6		GPIO4_I031	Default	3.3V	
				SAI3_TX_SYNC	ALT0		
				GPT1_CAPTURE2	ALT1		
				SAI5_RX_DATA[1]	ALT2		
				SAI3_TX_DATA[1]	ALT3		
				UART2_RX	ALT4		
				GPIO4_I031	ALT5		
34	DSI_TP_RSTLV	AB10		GPIO1_I012	Default	1.8V	
				GPIO1_I0012	ALT0		
				USB1_OTG_PWR	ALT1		
				SDMA2_EXT_EVENT[1]	ALT5		

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				Signal Name	Mode		
	35	SAI3_TXC	AG6	GPI05_I000	Default	3.3V	
				SAI3_TX_BCLK	ALT0		
				GPT1_COMPARE2	ALT1		
				SAI5_RX_DATA[2]	ALT2		
				UART2_TX	ALT4		
				GPI05_I000	ALT5		
	36	CSI_P1_I01	AC10	GPI01_I011	Default	1.8V	
				GPI01_I0011	ALT0		
				USB2_OTG_ID	ALT1		
				USDHC3_VSELECT	ALT4		
				CCMSRCGPCMIX_PMIC_READY	ALT5		
	37	SAI3_TXD	AF6	GPI05_I001	Default	3.3V	
				SAI3_TX_DATA[0]	ALT0		
				GPT1_COMPARE3	ALT1		
				SAI5_RX_DATA[3]	ALT2		
				GPI05_I001	ALT5		
	38	GPI01_09LV	AF10	GPI01_I009	Default	1.8V	
				GPI01_I009	ALT0		
				USB2_OTG_ID	ALT1		
				USDHC3_VSELECT	ALT4		
				CCMSRCGPCMIX_PMIC_READY	ALT5		
	39	SAI3_RXFS	AG8	GPI04_I028	Default	3.3V	
				SAI3_RX_SYNC	ALT0		
				GPT1_CAPTURE1	ALT1		
				SAI5_RX_SYNC	ALT2		
				SAI3_RX_DATA[1]	ALT3		
				GPI04_I028	ALT5		
	40	DSI_BL_ENLV	AG10	GPI01_I008	Default	1.8V	
				GPI01_I008	ALT0		
				ENET1_1588_EVENT0_IN	ALT1		
				USDHC2_RESET_B	ALT5		
	41	SAI3_RXC	AG7	GPI04_I029	Default	3.3V	
				SAI3_RX_BCLK	ALT0		
				GPT1_CLK	ALT1		
				SAI5_RX_BCLK	ALT2		
				UART2_CTS_B	ALT4		
				GPI04_I029	ALT5		
	42	CSI_PWDN	AF11	GPI01_I007	Default	1.8V	
				GPI01_I007	ALT0		
				ENET1_MDIO	ALT1		
				USDHC1_WP	ALT5		
				CCMSRCGPCMIX_EXT_CLK4	ALT6		
	43	SAI3_RXD	AF7	GPI04_I030	Default	3.3V	
				SAI3_RX_DATA[0]	ALT0		
				GPT1_COMPARE1	ALT1		
				SAI5_RX_DATA[0]	ALT2		
				UART2_RTS_B	ALT4		
				GPI04_I030	ALT5		
	44	CSI_Nrst	AG11	GPI01_I006	Default	1.8V	
				GPI01_I006	ALT0		
				ENET1_MDC	ALT1		
				USDHC1_CD_B	ALT5		
				CCMSRCGPCMIX_EXT_CLK3	ALT6		
	45	LED2	AF9	GPI05_I003	Default	3.3V	
				SPDIF1_OUT	ALT0		
				PWM3_OUT	ALT1		
				GPI05_I003	ALT5		
	46	4G_RST	AF12	GPI01_I005	Default	1.8V	
				GPI01_I005	ALT0		
				M4_NM	ALT1		
				CCMSRCGPCMIX_PMIC_READY	ALT5		
47	LED1	AG9		GPI05_I004	Default	3.3V	
				SPDIF1_IN	ALT0		
				PWM2_OUT	ALT1		

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				Signal Name	Mode		
				GPI05_I004	ALT5		
	48	DSI_BL_PWMLV	AF14	GPI01_I001	Default	1.8V	
				GPI01_I001	ALT0		
				PWM1_OUT	ALT1		
				ANAMIX_REF_CLK_24M	ALT5		
				CCMSRCGPCMIX_EXT_CLK2	ALT6		
	49	SPDIF_EXT_CLK	AF8	GPI05_I005	Default	3.3V	
				SPDIF1_EXT_CLK	ALT0		
				PWM1_OUT	ALT1		
				GPI05_I005	ALT5		
	50	LVDS_IRQLV	AD19	GPI04_I027	Default	1.8V	
				SAI2_MCLK	ALT0		
				SAI5_MCLK	ALT1		
				GPI04_I027	ALT5		
	51	PDM_CLK	AC15	GPI03_I020	Default	3.3V	
				SAI5_RX_BCLK	ALT0		
				SAI1_TX_DATA[1]	ALT1		
				PDM_CLK	ALT4		
				GPI03_I020	ALT5		
	52	SAI2_RXFS	AC19	GPI04_I021	Default	1.8V	
				SAI2_RX_SYNC	ALT0		
				SAI5_TX_SYNC	ALT1		
				SAI5_RX_DATA[1]	ALT2		
				SAI2_RX_DATA1	ALT3		
				UART1_TX	ALT4		
				GPI04_IO[21]	ALT5		
	53	PDM_DATA0	AD18	GPI03_I021	Default	3.3V	
				SAI5_RX_DATA[0]	ALT0		
				SAI1_TX_DATA[2]	ALT1		
				PDM_BIT_STREAM[0]	ALT4		
				GPI03_I021	ALT5		
	54	KEY	AB15	GPI03_I019	Default	3.3V	10K pull up to 3.3V
				SAI5_RX_SYNC	ALT0		
				SAI1_TX_DATA[0]	ALT1		
				GPI03_I019	ALT5		
	55	PDM_DATA1	AC14	GPI03_I022	Default	3.3V	
				SAI5_RX_DATA[1]	ALT0		
				SAI1_TX_DATA[3]	ALT1		
				SAI1_TX_SYNC	ALT2		
				SAI5_TX_SYNC	ALT3		
				PDM_BIT_STREAM[1]	ALT4		
				GPI03_I022	ALT5		
	56	SAI1_RXFS	AG16	GPI04_I000	Default	3.3V	
				SAI1_RX_SYNC	ALT0		
				SAI5_RX_SYNC	ALT1		
				CORESIGHT_TRACE_CLK	ALT4		
				GPI04_I000	ALT5		
	57	PDM_DATA2	AD13	GPI03_I023	Default	3.3V	
				SAI5_RX_DATA[2]	ALT0		
				SAI1_TX_DATA[4]	ALT1		
				SAI1_TX_SYNC	ALT2		
				SAI5_TX_BCLK	ALT3		
				PDM_BIT_STREAM[2]	ALT4		
				GPI03_I023	ALT5		
	58	DSI_TS_nINT	AF16	GPI04_I001	Default	3.3V	10K pull up to 3.3V
				SAI1_RX_BCLK	ALT0		
				SAI5_RX_BCLK	ALT1		
				CORESIGHT_TRACE_CTL	ALT4		
				GPI04_I001	ALT5		
	59	PDM_DATA3	AC13	GPI03_I024	Default	3.3V	
				SAI5_RX_DATA[3]	ALT0		
				SAI1_TX_DATA[5]	ALT1		
				SAI1_TX_SYNC	ALT2		
				SAI5_TX_DATA[0]	ALT3		
				PDM_BIT_STREAM[3]	ALT4		

Ref	Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
				Signal Name	Mode		
				GPIO3_I024	ALT5		
	60	SAI1_RXD0	AG15	GPIO4_I002	Default	3.3V	
				SAI1_RX_DATA[0]	ALT0		
				SAI5_RX_DATA[0]	ALT1		
				SAI1_TX_DATA[1]	ALT2		
				PDM_BIT_STREAM[0]	ALT3		
				CORESIGHT_TRACE[0]	ALT4		
				GPIO4_I002	ALT5		
	61	SAI5_MCLK	AD15	GPIO3_I025	Default	3.3V	
				SAI5_MCLK	ALT0		
				SAI1_TX_BCLK	ALT1		
				GPIO3_I0[25]	ALT5		
	62	SAI1_RXD1	AF15	GPIO4_I003	Default	3.3V	
				SAI1_RX_DATA[1]	ALT0		
				SAI5_RX_DATA[1]	ALT1		
				PDM_BIT_STREAM[1]	ALT3		
				CORESIGHT_TRACE[1]	ALT4		
				GPIO4_I003	ALT5		
	63	SAI1_TXC	AC18	GPIO4_I011	Default	3.3V	
				SAI1_TX_BCLK	ALT0		
				SAI5_TX_BCLK	ALT1		
				CORESIGHT_EVENTI	ALT4		
				GPIO4_I011	ALT5		
	64	SAI1_RXD2	AG17	GPIO4_I004	Default	3.3V	
				SAI1_RX_DATA[2]	ALT0		
				SAI5_RX_DATA[2]	ALT1		
				PDM_BIT_STREAM[2]	ALT3		
				CORESIGHT_TRACE[2]	ALT4		
				GPIO4_I004	ALT5		
	65	SAI1_TXD0	AG20	GPIO4_I012	Default	3.3V	
				SAI1_TX_DATA[0]	ALT0		
				SAI5_TX_DATA[0]	ALT1		
				CORESIGHT_TRACE[8]	ALT4		
				GPIO4_I012	ALT5		
	66	SAI1_RXD3	AF17	GPIO4_I005	Default	3.3V	
				SAI1_RX_DATA[3]	ALT0		
				SAI5_RX_DATA[3]	ALT1		
				PDM_BIT_STREAM[3]	ALT3		
				CORESIGHT_TRACE[3]	ALT4		
				GPIO4_I005	ALT5		
	67	SAI1_TXD1	AF20	GPIO4_I013	Default	3.3V	
				SAI1_TX_DATA[1]	ALT0		
				SAI5_TX_DATA[1]	ALT1		
				CORESIGHT_TRACE[9]	ALT4		
				GPIO4_I013	ALT5		
	68	SAI1_RXD4	AG18	GPIO4_I006	Default	3.3V	
				SAI1_RX_DATA[4]	ALT0		
				SAI6_TX_BCLK	ALT1		
				SAI6_RX_BCLK	ALT2		
				CORESIGHT_TRACE[4]	ALT4		
				GPIO4_I006	ALT5		
	69	SAI1_TXD2	AG21	GPIO4_I014	Default	3.3V	
				SAI1_TX_DATA[2]	ALT0		
				SAI5_TX_DATA[2]	ALT1		
				CORESIGHT_TRACE[10]	ALT4		
				GPIO4_I014	ALT5		
	70	SAI1_RXD5	AF18	GPIO4_I007	Default	3.3V	
				SAI1_RX_DATA[5]	ALT0		
				SAI6_TX_DATA[0]	ALT1		
				SAI6_RX_DATA[0]	ALT2		
				SAI1_RX_SYNC	ALT3		
				CORESIGHT_TRACE[5]	ALT4		
				GPIO4_I007	ALT5		
				GPIO4_I015	Default		

Ref	Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
				Signal Name	Mode		
	71	SAI1_TXD3	AF21	SAI1_TX_DATA[3]	ALT0	3.3V	
				SAI5_TX_DATA[3]	ALT1		
				CORESIGHT_TRACE[11]	ALT4		
				GPIO4_I015	ALT5		
	72	SAI1_RXD6	AG19	GPIO4_I008	Default	3.3V	
				SAI1_RX_DATA[6]	ALT0		
				SAI6_TX_SYNC	ALT1		
				SAI6_RX_SYNC	ALT2		
				CORESIGHT_TRACE[6]	ALT4		
				GPIO4_I008	ALT5		
	73	SAI1_TXD4	AG22	GPIO4_I016	Default	3.3V	
				SAI1_TX_DATA[4]	ALT0		
				SAI6_RX_BCLK	ALT1		
				SAI6_TX_BCLK	ALT2		
				CORESIGHT_TRACE[12]	ALT4		
				GPIO4_I016	ALT5		
	74	SAI1_RXD7	AF19	GPIO4_I009	Default	3.3V	
				SAI1_RX_DATA[7]	ALT0		
				SAI6_MCLK	ALT1		
				SAI1_TX_SYNC	ALT2		
				SAI1_TX_DATA[4]	ALT3		
				CORESIGHT_TRACE[7]	ALT4		
				GPIO4_I009	ALT5		
	75	SAI1_TXD5	AF22	GPIO4_I017	Default	3.3V	
				SAI1_TX_DATA[5]	ALT0		
				SAI6_RX_DATA[0]	ALT1		
				SAI6_TX_DATA[0]	ALT2		
				CORESIGHT_TRACE[13]	ALT4		
				GPIO4_I017	ALT5		
	76	SAI1_MCLK	AB18	GPIO4_I020	Default	3.3V	
				SAI1_MCLK	ALT0		
				SAI5_MCLK	ALT1		
				SAI1_TX_BCLK	ALT2		
				PDM_CLK	ALT3		
				GPIO4_I020	ALT5		
	77	SAI1_TXD6	AG23	GPIO4_I018	Default	3.3V	
				SAI1_TX_DATA[6]	ALT0		
				SAI6_RX_SYNC	ALT1		
				SAI6_TX_SYNC	ALT2		
				CORESIGHT_TRACE[14]	ALT4		
				GPIO4_I018	ALT5		
	78	SAI1_TXFS	AB19	GPIO4_I010	Default	3.3V	
				SAI1_TX_SYNC	ALT0		
				SAI5_TX_SYNC	ALT1		
				CORESIGHT_EVENT0	ALT4		
				GPIO4_I010	ALT5		
	79	SAI1_TXD7	AF23	GPIO4_I019	Default	3.3V	
				SAI1_TX_DATA[7]	ALT0		
				SAI6_MCLK	ALT1		
				PDM_CLK	ALT3		
				CORESIGHT_TRACE[15]	ALT4		
				GPIO4_I019	ALT5		
	80	SAI2_TXFS	AD23	GPIO4_I024	Default	1.8V	
				SAI2_TX_SYNC	ALT0		
				SAI5_TX_DATA[1]	ALT1		
				SAI2_TX_DATA[1]	ALT3		
				UART1_CTS_B	ALT4		
				GPIO4_I024	ALT5		
81	NVCC_SD2					3.3V/1.8V	Built in PMU chip(BD71847MWV) pin
82	DGND						
	83	SAI2_TXD	AC22	GPIO4_I026	Default	1.8V	
				SAI2_TX_DATA[0]	ALT0		
				SAI5_TX_DATA[3]	ALT1		
				GPIO4_I026	ALT5		

Ref	Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
				Signal Name	Mode		
	84	ETH_TRP0					Built in 以太网 chip(AR8035) pin
	85	SAI2_TXC	AD22	GPI04_I025	Default	1.8V	
				SAI2_TX_BCLK	ALT0		
				SAI5_TX_DATA[2]	ALT1		
				GPI04_I025	ALT5		
	86	ETH_TRN0					Built in 以太网 chip(AR8035) pin
	87	SAI2_RXD	AC24	GPI04_I023	Default	1.8V	
				SAI2_RX_DATA[0]	ALT0		
				SAI5_TX_DATA[0]	ALT1		
				UART1_RTS_B	ALT4		
				GPI04_I023	ALT5		
	88	DGND					
	89	ENET_LED_ACT					Built in Ethernet chip(AR8035) pin
	90	ETH_TRP1					
	91	ENET_LED_1000					Built in Ethernet chip(AR8035) pin
	92	ETH_TRN1					Built in Ethernet chip(AR8035) pin
	93	DGND					
	94	DGND					
	95	ETH_TRP3					Built in Ethernet chip(AR8035) pin
	96	ETH_TRP2					Built in Ethernet chip(AR8035) pin
	97	ETH_TRN3					Built in Ethernet chip(AR8035) pin
	98	ETH_TRN2					Built in Ethernet chip(AR8035) pin
	99	DGND					
	100	DGND					
J2	1	UART3_RTS	B6	GPI05_I009	Default	3.3V	
				ECSP11_SS0	ALT0		
				UART3_RTS_B	ALT1		
				GPI05_I009	ALT5		
	2	DGND					
	3	UART3_RXD	D6	GPI05_I006	Default	3.3V	
				ECSP11_SCLK	ALT0		
				UART3_RX	ALT1		
				GPI05_I006	ALT5		
	4	DSI_DN0	A9				
	5	UART3_TXD	B7	GPI05_I007	Default	3.3V	
				ECSP11_MOSI	ALT0		
				UART3_TX	ALT1		
				GPI05_I007	ALT5		
	6	DSI_DP0	B9				
	7	DGND					
	8	DGND					
	9	UART3_CTS	A7	GPI05_I008	Default	3.3V	
				ECSP11_MISO	ALT0		
				UART3_CTS_B	ALT1		
				GPI05_I008	ALT5		
	10	DSI_DN1	A10				
	11	ECSP12_SS0	A6	GPI05_I013	Default	3.3V	
				ECSP12_SS0	ALT0		
				UART4_RTS_B	ALT1		
				GPI05_I013	ALT5		
	12	DSI_DP1	B10				
	13	ECSP12_SCLK	E6	GPI05_I010	Default	3.3V	
				ECSP12_SCLK	ALT0		
				UART4_RX	ALT1		
				GPI05_I010	ALT5		
	14	DGND					
	15	ECSP12_MISO	A8	GPI05_I012	Default	3.3V	
				ECSP12_MISO	ALT0		
				UART4_CTS_B	ALT1		
				GPI05_I012	ALT5		
	16	DSI_CKN	A11				
	17	ECSP12_MOSI	D8	GPI05_I011	Default	3.3V	
				ECSP12_MOSI	ALT0		

Ref	Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
				Signal Name	Mode		
	17	ECSP12_MISO1	D8	UART4_TX	ALT1	3.3V	
				GPIO5_IO11	ALT5		
	18	DSI_CKP	B11				
	19	DGND					
	20	DGND					
	21	I2C2_SCL	D10	GPIO5_IO16	Default	1.8V	
				I2C2_SCL	ALT0		
				ENET1_1588_EVENT1_IN	ALT1		
				USDHC3_CD_B	ALT2		
				GPIO5_IO16	ALT5		
	22	DSI_DN2	A12				
	23	I2C2_SDA	D9	GPIO5_IO17	Default	1.8V	
				I2C2_SDA	ALT0		
				ENET1_1588_EVENT1_OUT	ALT1		
				USDHC3_WP	ALT2		
				GPIO5_IO17	ALT5		
	24	DSI_DP2	B12				
	25	I2C3_SCL	E10	GPIO5_IO18	Default	1.8V	
				I2C3_SCL	ALT0		
				PWM4_OUT	ALT1		
				GPT2_CLK	ALT2		
				GPIO5_IO18	ALT5		
	26	DGND					
	27	I2C3_SDA	F10	GPIO5_IO19	Default	1.8V	
				I2C3_SDA	ALT0		
				PWM3_OUT	ALT1		
				GPT3_CLK	ALT2		
				GPIO5_IO19	ALT5		
	28	DSI_DN3	A13				
	29	I2C4_SCL	D13	GPIO5_IO20	Default	1.8V	
				I2C4_SCL	ALT0		
				PWM2_OUT	ALT1		
				PCIE1_CLKREQ_B	ALT2		
				GPIO5_IO20	ALT5		
	30	DSI_DP3	B13				
	31	I2C4_SDA	E13	GPIO5_IO21	Default	1.8V	
				I2C4_SDA	ALT0		
				PWM1_OUT	ALT1		
				GPIO5_IO21	ALT5		
	32	DGND					
	33	UART2_RXD	F15	GPIO5_IO24	Default	1.8V	
				UART2_RX	ALT0		
				ECSP13_MISO	ALT1		
				GPIO5_IO24	ALT5		
	34	CSI_DN0	A14				
	35	UART2_TXD	E15	GPIO5_IO25	Default	1.8V	
				UART2_TX	ALT0		
				ECSP13_SS0	ALT1		
				GPIO5_IO25	ALT5		
	36	CSI_DP0	B14				
	37	UART4_RXD	F19	GPIO5_IO28	Default	1.8V	
				UART4_RX	ALT0		
				UART2_CTS_B	ALT1		
				PCIE1_CLKREQ_B	ALT2		
				GPIO5_IO28	ALT5		
	38	DGND					
	39	UART4_TXD	F18	GPIO5_IO29	Default	1.8V	
				UART4_TX	ALT0		
				UART2_RTS_B	ALT1		
				GPIO5_IO29	ALT5		
	40	CSI_DN1	A15				
	41	ONOFF	A25				
	42	CSI_DP1	B15				

Ref	Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
				Signal Name	Mode		
	43	PMIC_ON_REQ	A24				
	44	DGND					
	45	POR_B	B24				
	46	CSI_CKN	A16				
	47	SD1_STROBE	R24	GPI02_I011	Default	3.3V	
				USDHC1_STROBE	ALT0		
				GPI02_I011	ALT5		
	48	CSI_CKP	B16				
	49	SD2_DATA2	V24	GPI02_I017	Default	3.3V/1.8V	
				USDHC2_DATA2	ALT0		
				GPI02_I017	ALT5		
	50	DGND					
	51	SD2_DATA3	V23	GPI02_I018	Default	3.3V/1.8V	
				USDHC2_DATA3	ALT0		
				GPI02_I018	ALT5		
	52	CSI_DN2	A17				
	53	SD2_CLK	W23	GPI02_I013	Default	3.3V/1.8V	
				USDHC2_CLK	ALT0		
				GPI02_I013	ALT5		
	54	CSI_DP2	B17				
	55	SD2_CMD	W24	GPI02_I014	Default	3.3V/1.8V	
				USDHC2_CMD	ALT0		
				GPI02_I014	ALT5		
	56	DGND					
	57	SD2_DATA1	AB24	GPI02_I016	Default	3.3V/1.8V	
				USDHC2_DATA1	ALT0		
				GPI02_I016	ALT5		
	58	CSI_DN3	A18				
	59	SD2_DATA0	AB23	GPI02_I015	Default	3.3V/1.8V	
				USDHC2_DATA0	ALT0		
				GPI02_I015	ALT5		
	60	CSI_DP3	B18				
	61	SD2_WP	AA27	GPI02_I020	Default	3.3V/1.8V	
				USDHC2_WP	ALT0		
				GPI02_I020	ALT5		
	62	DGND					
	63	SD2_nCD	AA26	GPI02_I012	Default	3.3V/1.8V	
				USDHC2_CD_B	ALT0		
				GPI02_I012	ALT5		
	64	PCIE_RXN	A19				
	65	SD2_nRST	AB26	GPI02_I019	Default	3.3V/1.8V	
				USDHC2_RESET_B	ALT0		
				GPI02_I019	ALT5		
	66	PCIE_RXP	B19				
	67	BOOT_MODE0				1.8V	4.7K pull up to 1.8V
	68	DGND					
	69	BOOT_MODE1				1.8V	4.7K pull up to 1.8V
	70	PCIE_TXN	A20				
	71	UART1_CTS	E18	GPI05_I026	Default	1.8V	
				UART3_RX	ALT0		
				UART1_CTS_B	ALT1		
				USDHC3_RESET_B	ALT2		
				GPI05_I026	ALT5		
	72	PCIE_TXP	B20				
	73	UART1_TXD	F13	GPI05_I023	Default	1.8V	
				UART1_TX	ALT0		
				ECSP13_MOSI	ALT1		
				GPI05_I023	ALT5		
	74	DGND					
	75	UART1_RXD	E14	GPI05_I022	Default	1.8V	
				UART1_RX	ALT0		
				ECSP13_SCLK	ALT1		
				GPI05_I022	ALT5		

Ref	Num	Pin Name	BGA621 Ball	Pin Multiplexer		Power Rail	Comment
				Signal Name	Mode		
	76	PCIE_CLKN	A21				
	77	UART1_RTS	D18	GPIO5_I027	Default	1.8V	
				UART3_TX	ALT0		
				UART1_RTS_B	ALT1		
				USDHC3_VSELECT	ALT2		
				GPIO5_I027	ALT5		
	78	PCIE_CLKP	B21				
	79	SD1_DATA0	Y27	GPIO2_I002	Default	3.3V	
				USDHC1_DATA0	ALT0		
				GPIO2_I002	ALT5		
	80	DGND					
	81	SD1_DATA1	Y26	GPIO2_I003	Default	3.3V	
				USDHC1_DATA1	ALT0		
				GPIO2_I003	ALT5		
	82	USB1_VBUS	F22			3.3V	
	83	SD1_DATA2	T27	GPIO2_I004	Default	3.3V	
				USDHC1_DATA2	ALT0		
				GPIO2_I004	ALT5		
	84	USB_OTG1_DN	A22				
	85	SD1_DATA3	T26	GPIO2_I005	Default	3.3V	
				USDHC1_DATA3	ALT0		
				GPIO2_I005	ALT5		
	86	USB_OTG1_DP	B22				
	87	SD1_CMD	V27	GPIO2_I001	Default	3.3V	
				USDHC1_CMD	ALT0		
				GPIO2_I001	ALT5		
	88	DGND					
	89	SD1_CLK	V26	GPIO2_I000	Default	3.3V	
				USDHC1_CLK	ALT0		
				GPIO2_I000	ALT5		
	90	USB1_ID	D22				
	91	WL_REG_ON	R23	GPIO2_I010	Default	3.3V	
				USDHC1_RESET_B	ALT0		
				GPIO2_I010	ALT5		
	92	USB2_VBUS	F23				
	93	WL_WAKE_HOST	W26	GPIO2_I009	Default	3.3V	
				USDHC1_DATA7	ALT0		
				GPIO2_I009	ALT5		
	94	USB_OTG2_DN	A23				
	95	BT_REG_ON	U27	GPIO2_I006	Default	3.3V	
				USDHC1_DATA4	ALT0		
				GPIO2_I006	ALT5		
	96	USB_OTG2_DP	B23				
	97	BT_WAKE_DEV	U26	GPIO2_I007	Default	3.3V	
				USDHC1_DATA5	ALT0		
				GPIO2_I007	ALT5		
	98	DGND					
	99	BT_WAKE_HOST	W27	GPIO2_I008	Default	3.3V	
				USDHC1_DATA6	ALT0		
				GPIO2_I008	ALT5		
	100	USB2_ID	D23				