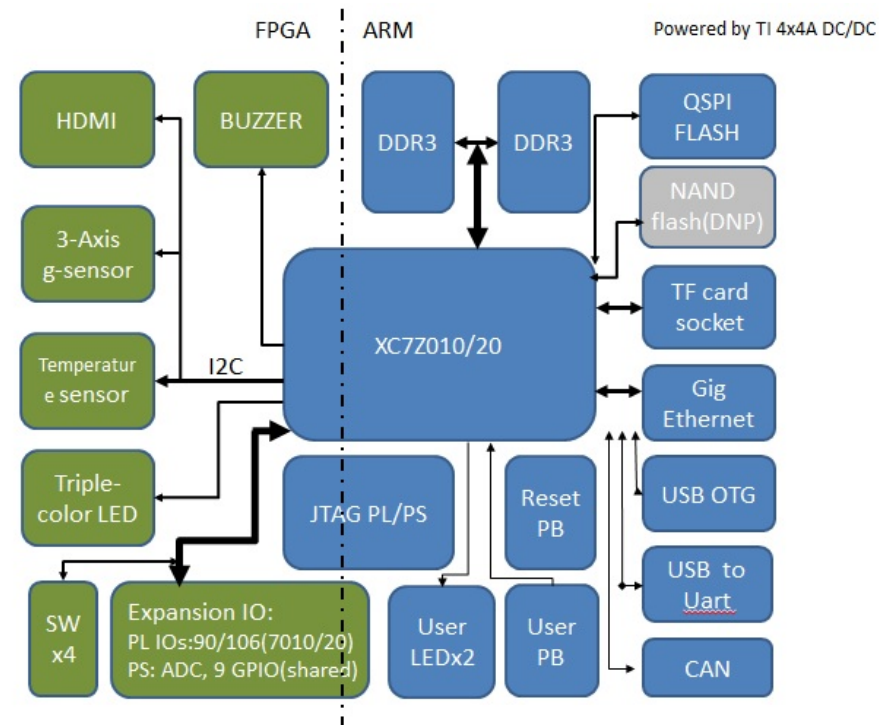
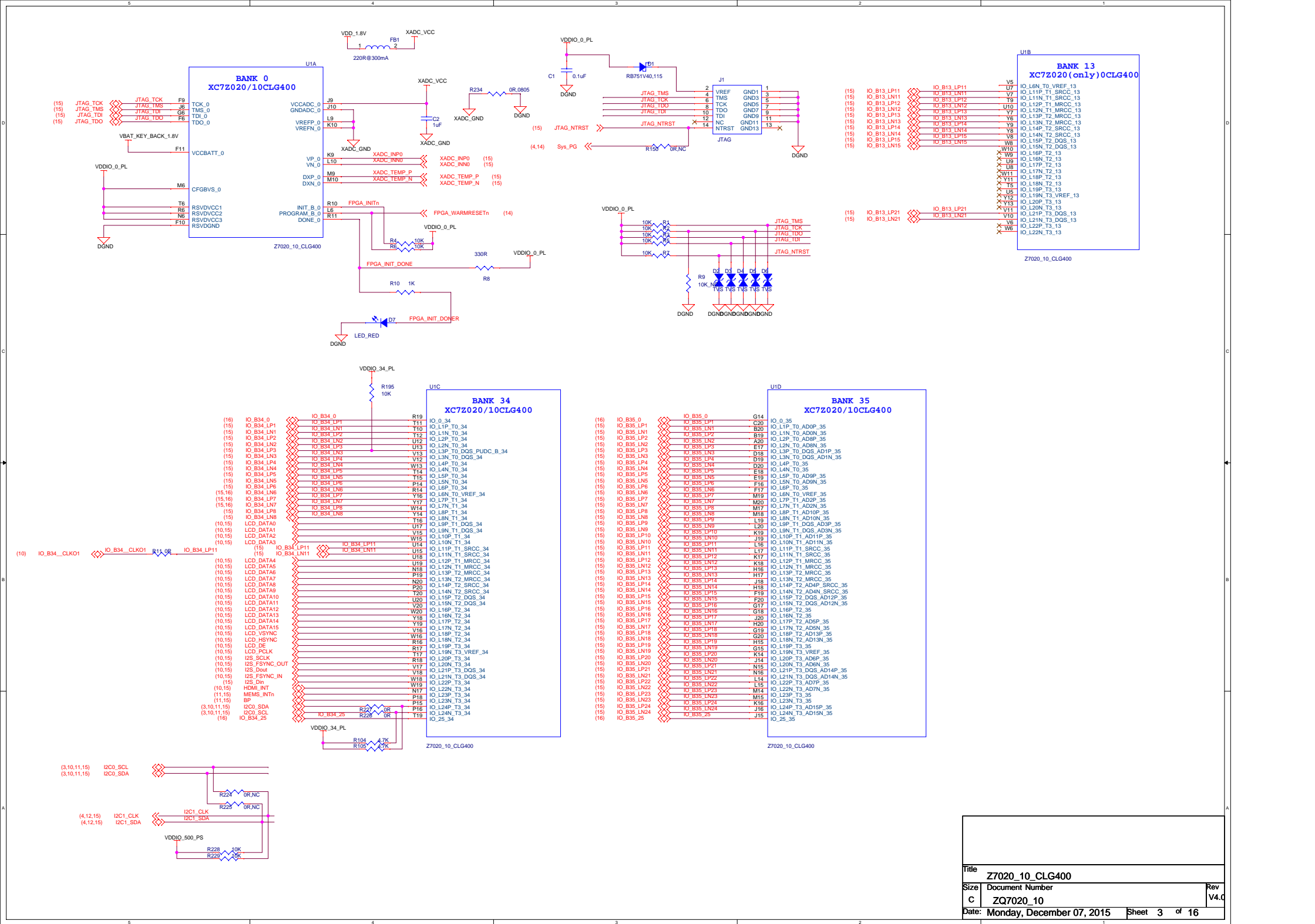


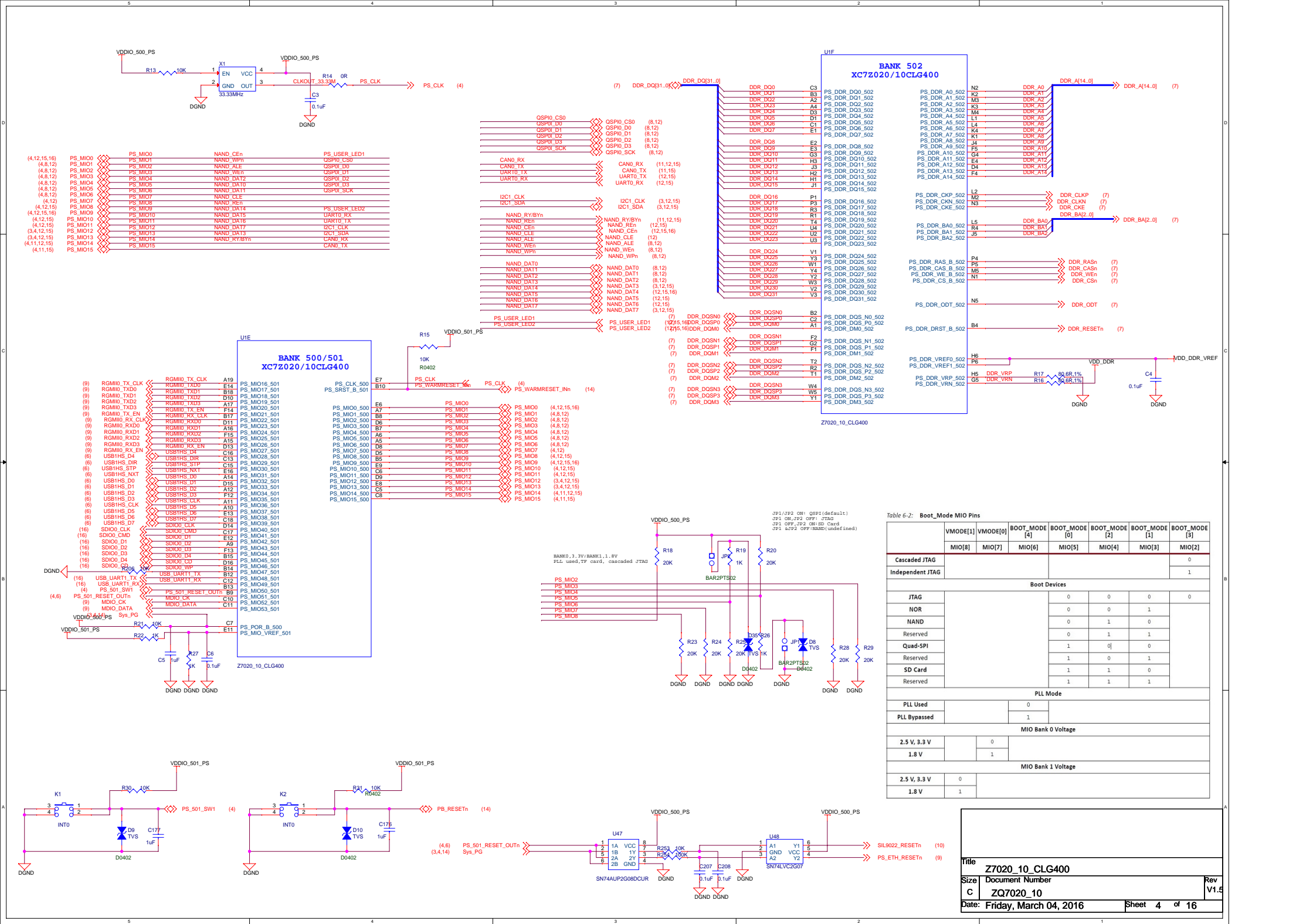
REV	Description	DATE	BY
Ver 1	Initial Release.		M
Ver 2	1 Replace HDMI Frame and SDIO Port voltage-level translation 2 Replaced USB2UART Chip		M
Ver 3	1 Add MDIO voltage-level translator 2 Modify CAN RX PULL voltage		
Ver 4	1 ADD GND_ADC to DGND 20150220 2 update sch version 3 clean sch 20150526		
Ver 5	1 replace the ethernetPhy		

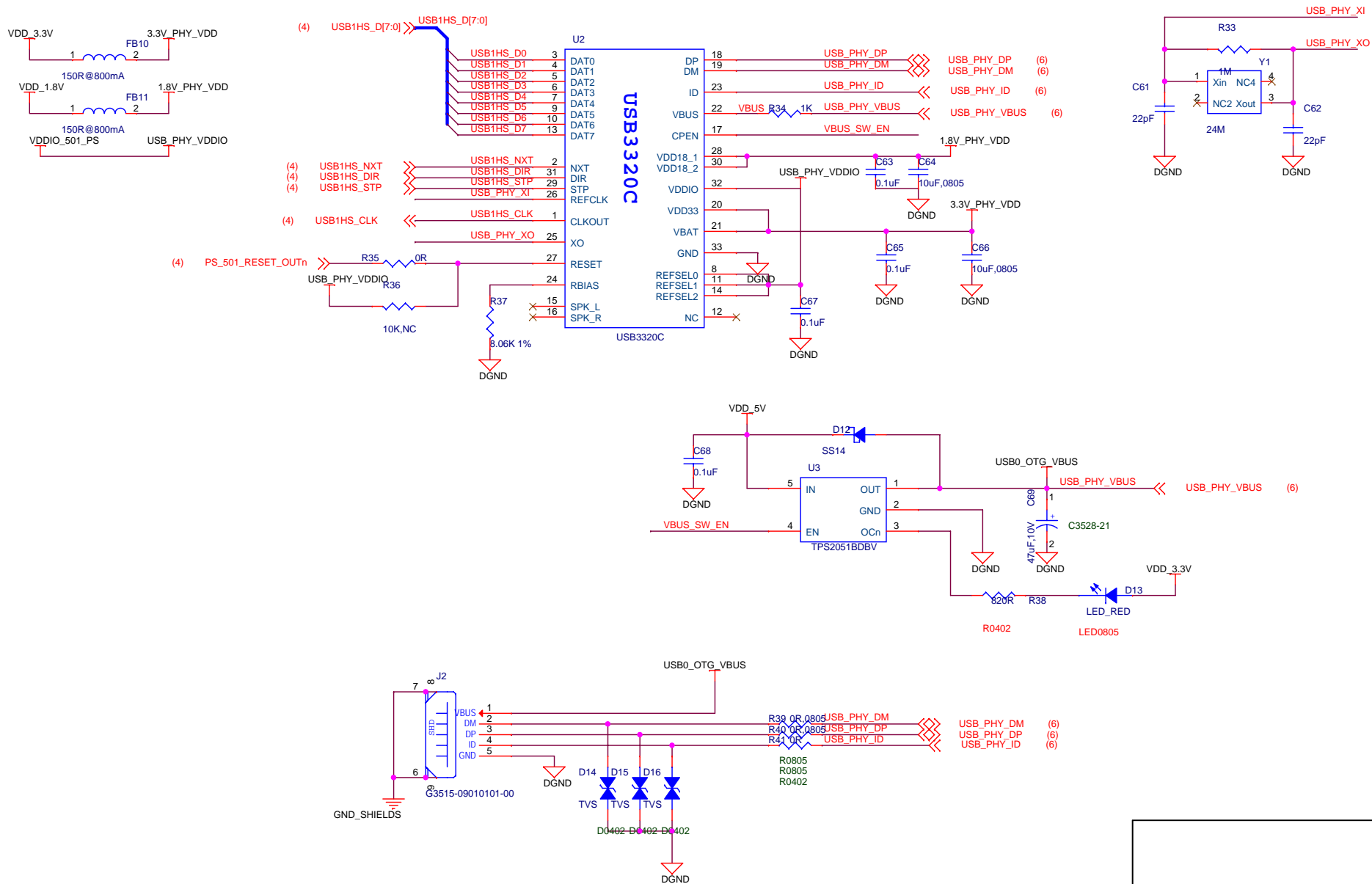
PAGE NO.	SCHEMATIC PAGE
1	TITLE PAGE
2	BLOCK
3	Z7020_PL
4	Z7020_PS
5	Z7020_POWER
6	USB OTG
7	DDR3 MEMORY
8	QSPI FLASH
9	10/100/1000 ETHERNET
10	HDMI
11	Sensor: G&T,CAN
12	NAND_FLASH
13	PWR:1.0V&1.5V,VTT
14	Power 1.8V,3.3V&DCin
15	EXP CONN
16	MISC



Title		
Z7020_10_CLG400		
Size	Document Number	
B	ZQ7020_10	
Date:	Friday, March 04, 2016	Sheet 2 of 16
Rev		V1.5

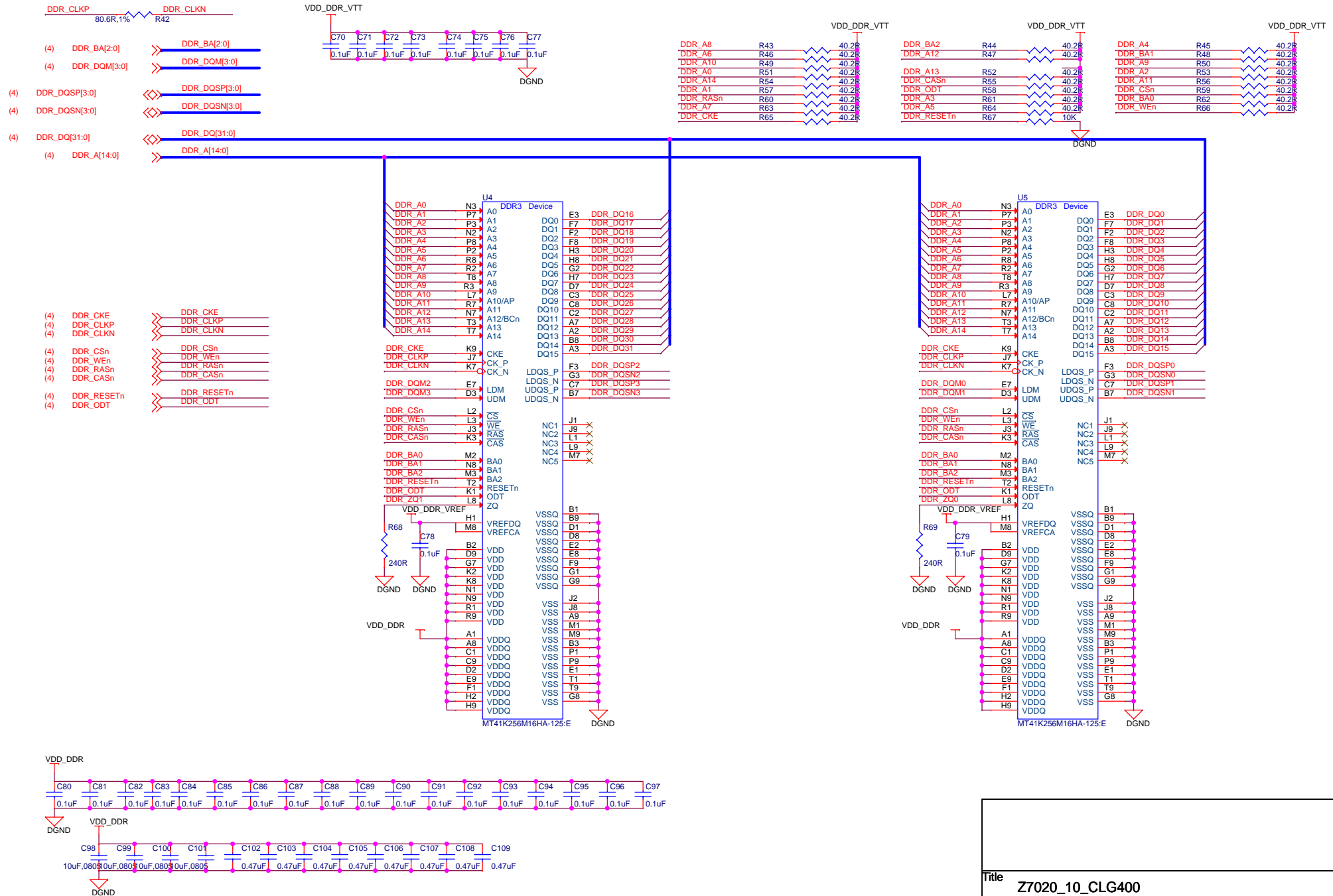




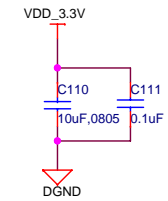
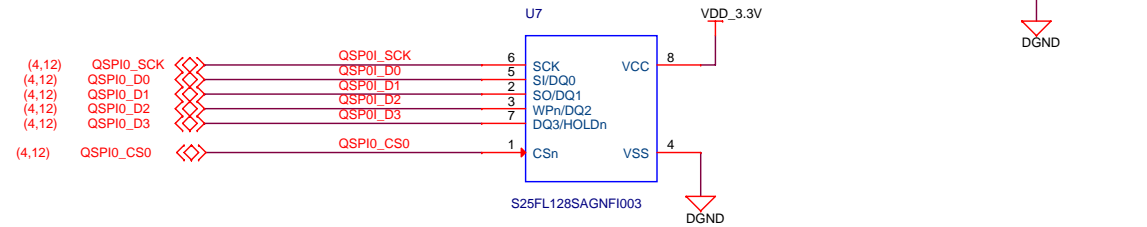


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Size	Document Number		Rev
B	ZQ7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 6 of 16

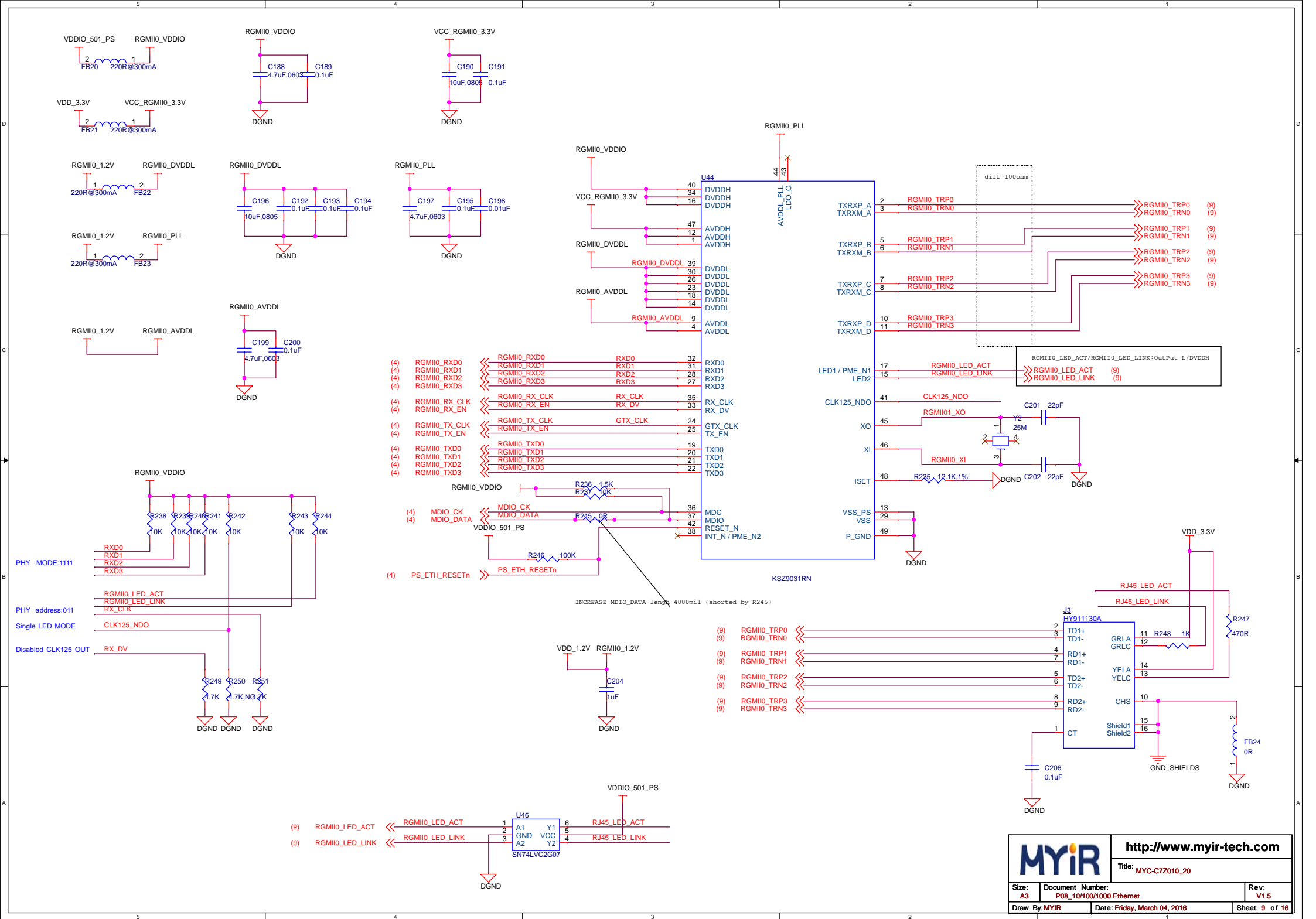
1024MB DDR3 (x32)

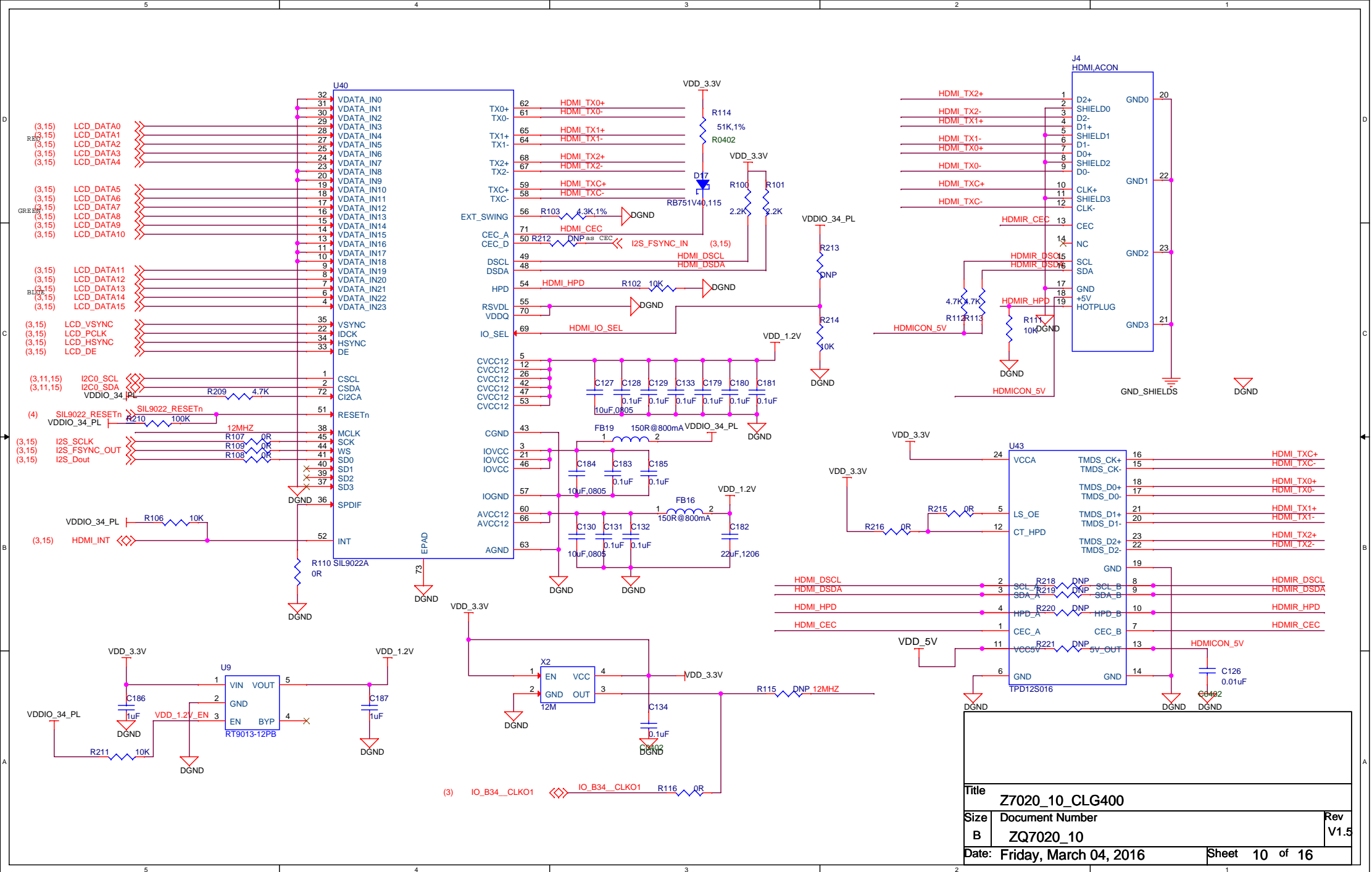


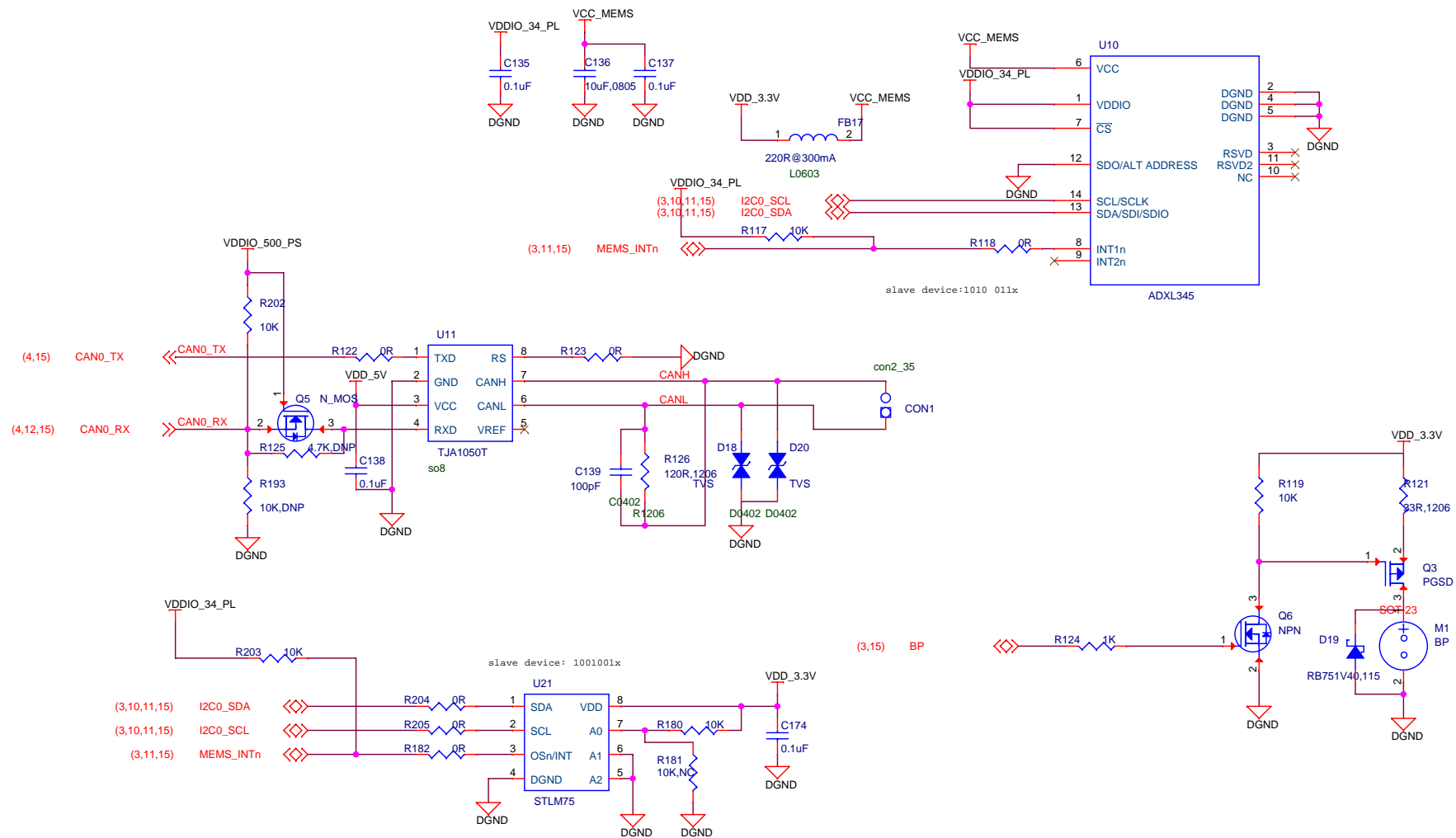
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Size	Document Number	Rev
A3	ZQ7020_10	V1.5
Date:	Friday, March 04, 2016	Sheet 7 of 16



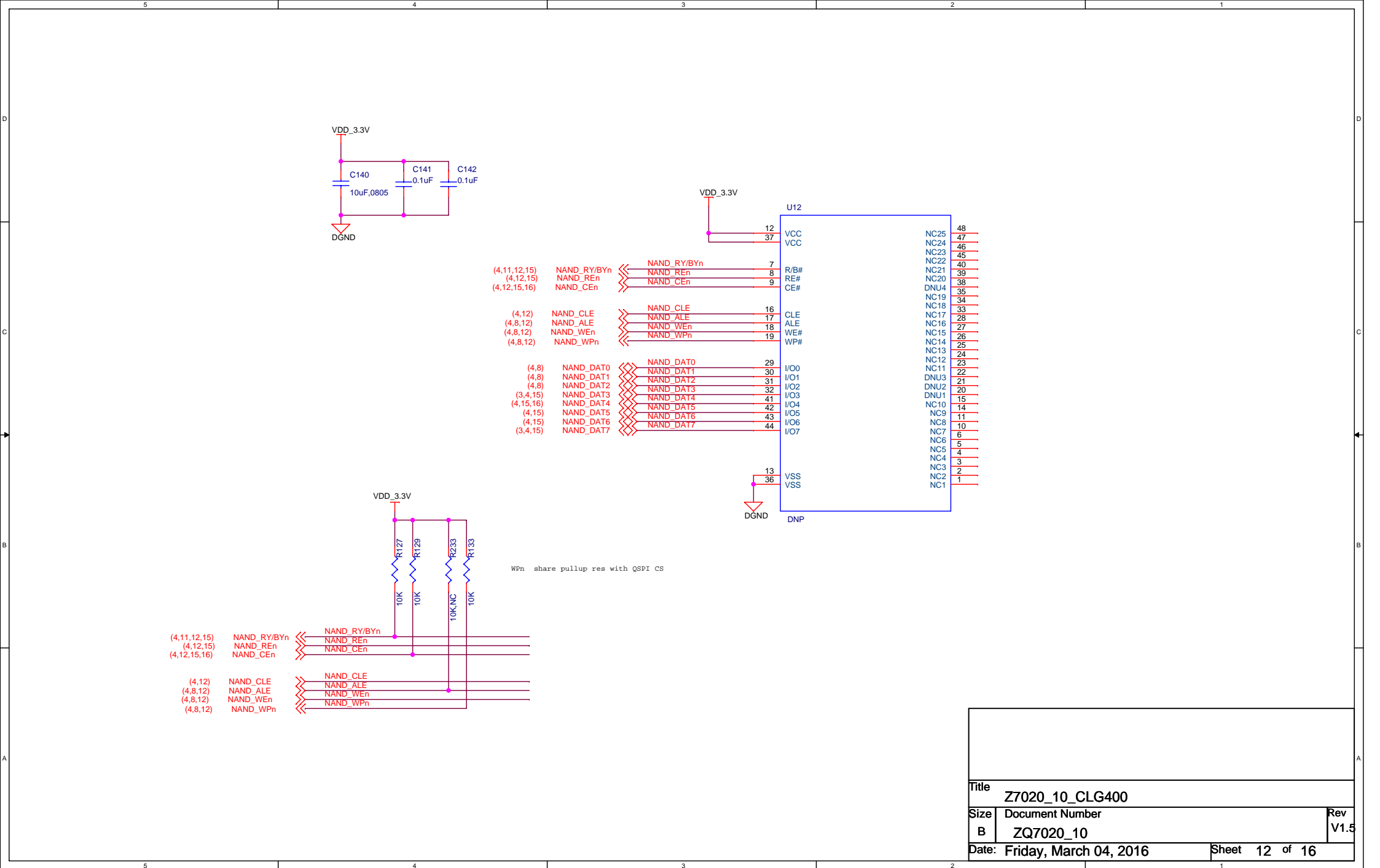
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Z7020_10_CLG400			
Size	Document Number		Rev
B	ZQ7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 8 of 16



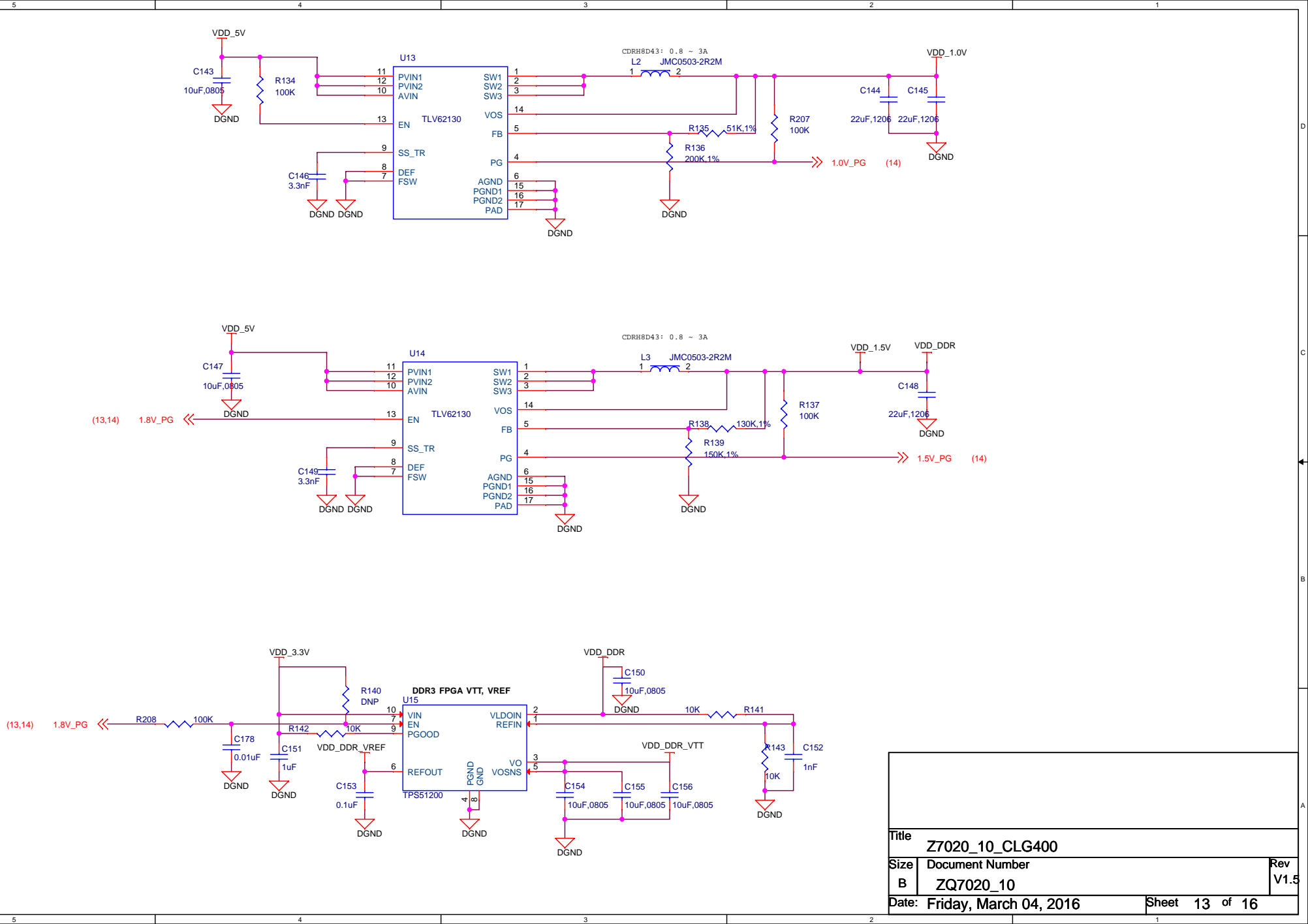




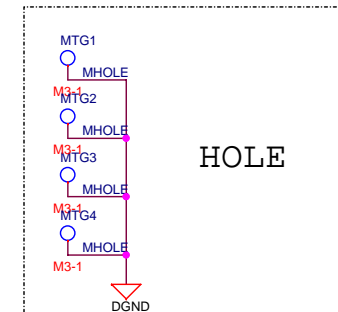
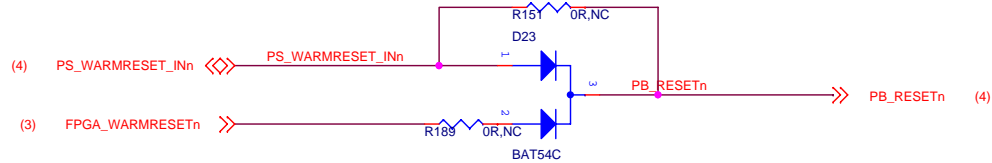
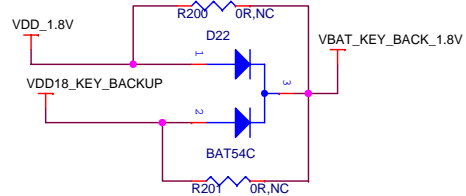
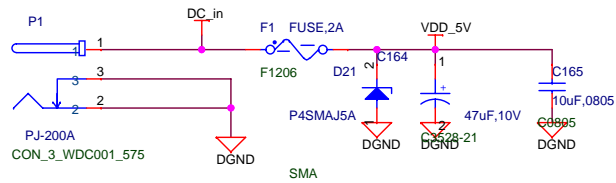
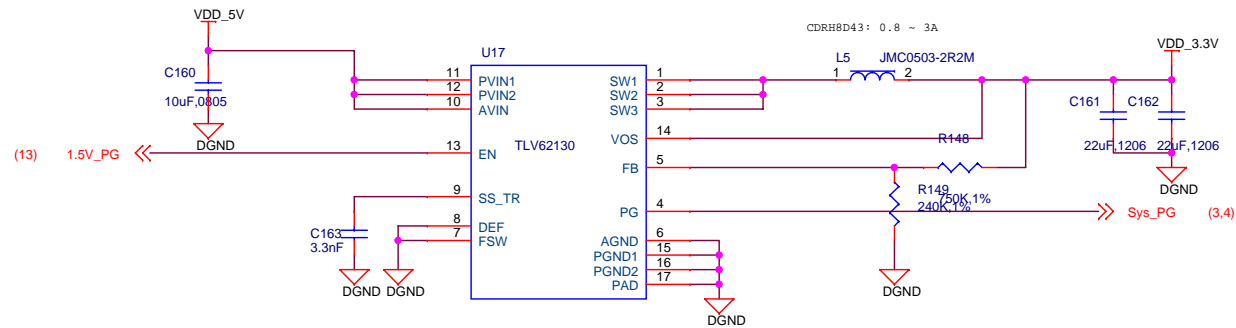
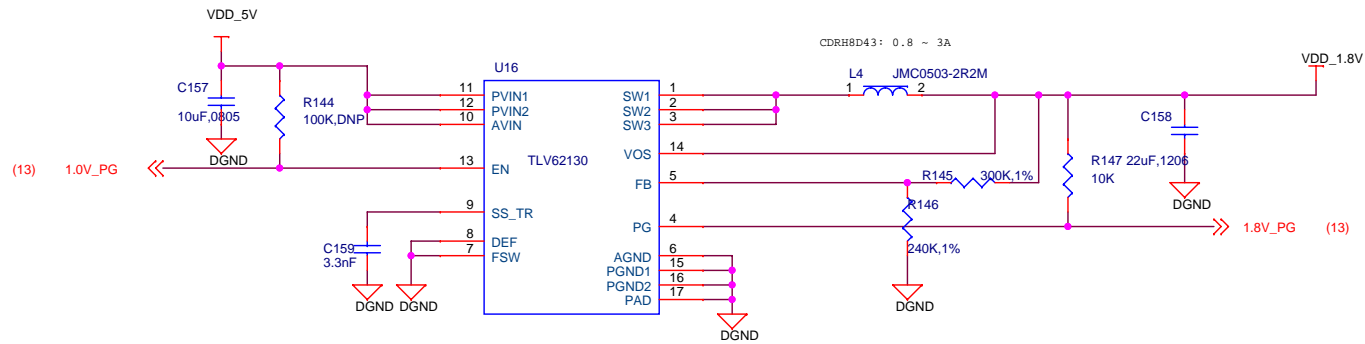
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Size	Document Number		Rev
B	ZQ7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 11 of 16



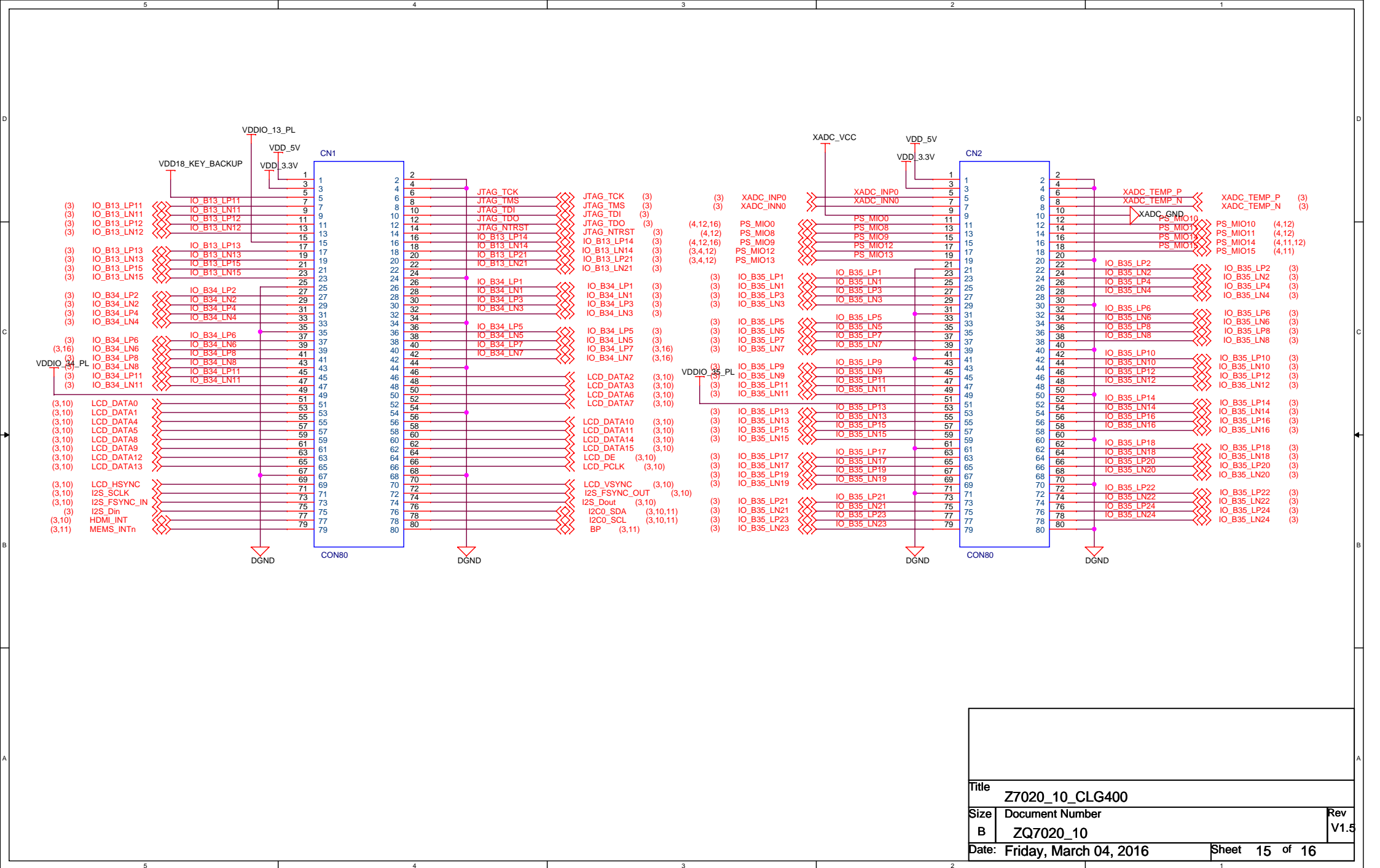
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Size	Document Number		Rev
B	ZQ7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 12 of 16



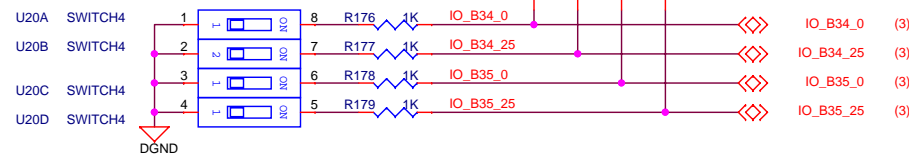
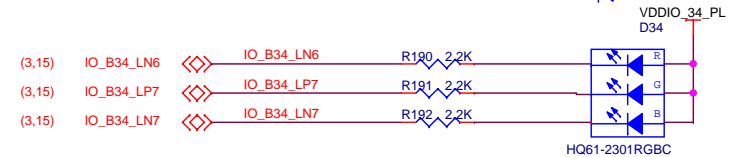
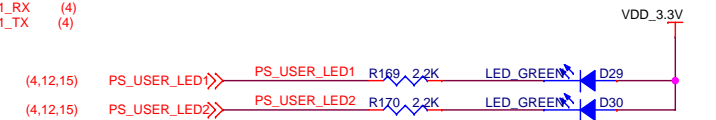
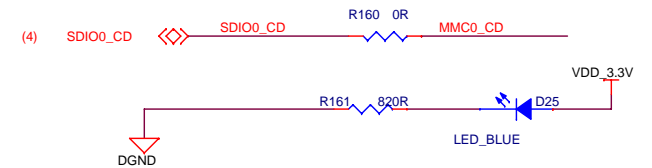
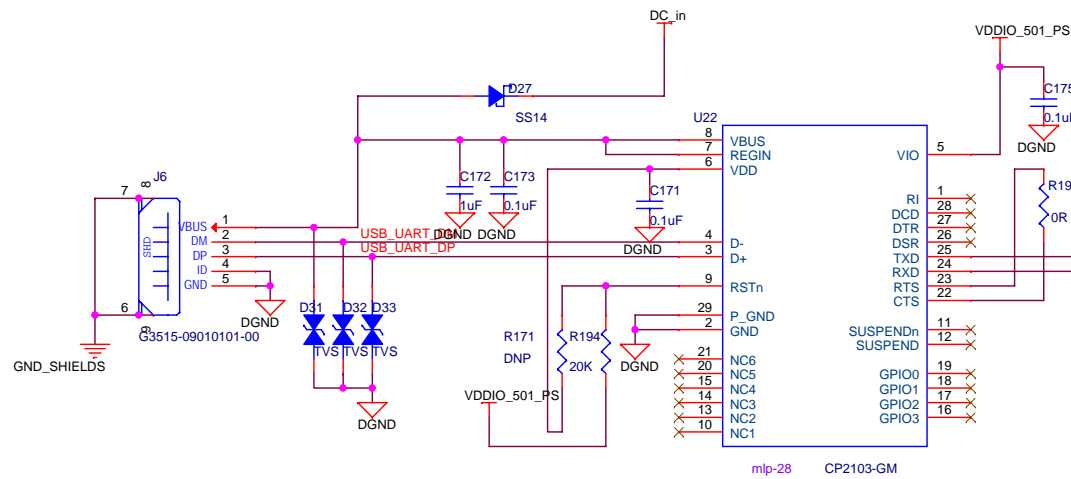
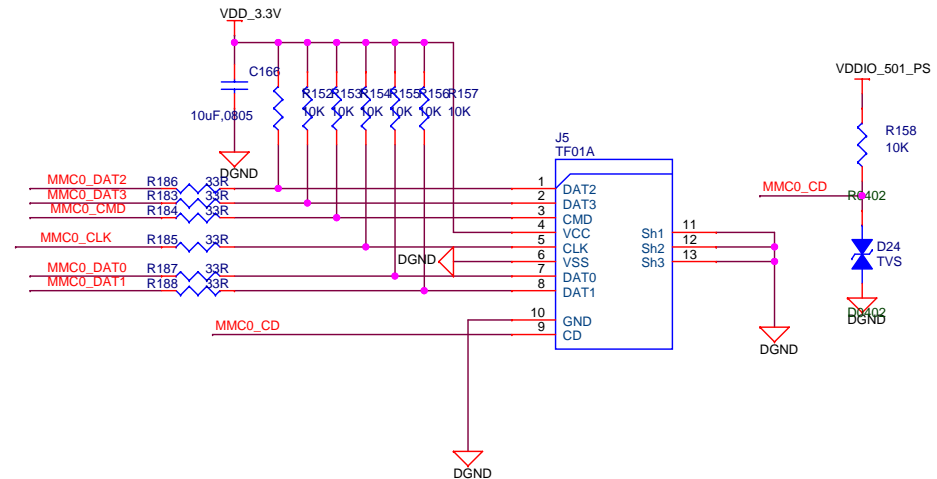
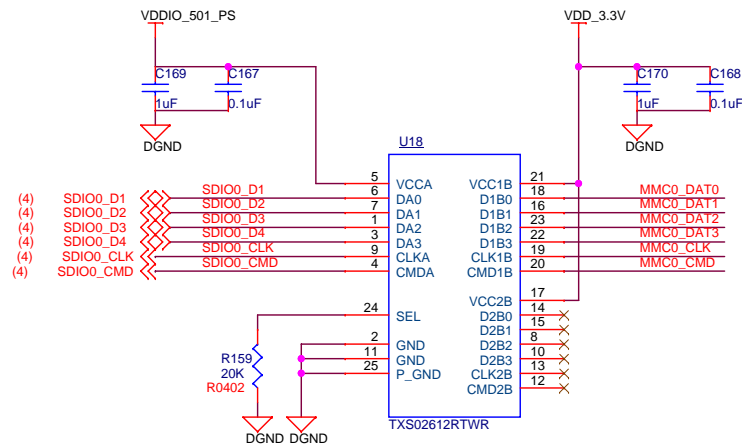
Title			Z7020_10_CLG400
Size	Document Number		Rev
B	ZQ7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 13 of 16



Title			Z7020_10_CLG400
Size	Document Number		Rev
B	Z7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 14 of 16



Title			Z7020_10_CLG400
Size	Document Number		Rev
B	ZQ7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 15 of 16



Title			Z7020_10_CLG400
Size	Document Number		Rev
B	ZQ7020_10		V1.5
Date:	Friday, March 04, 2016		Sheet 16 of 16