

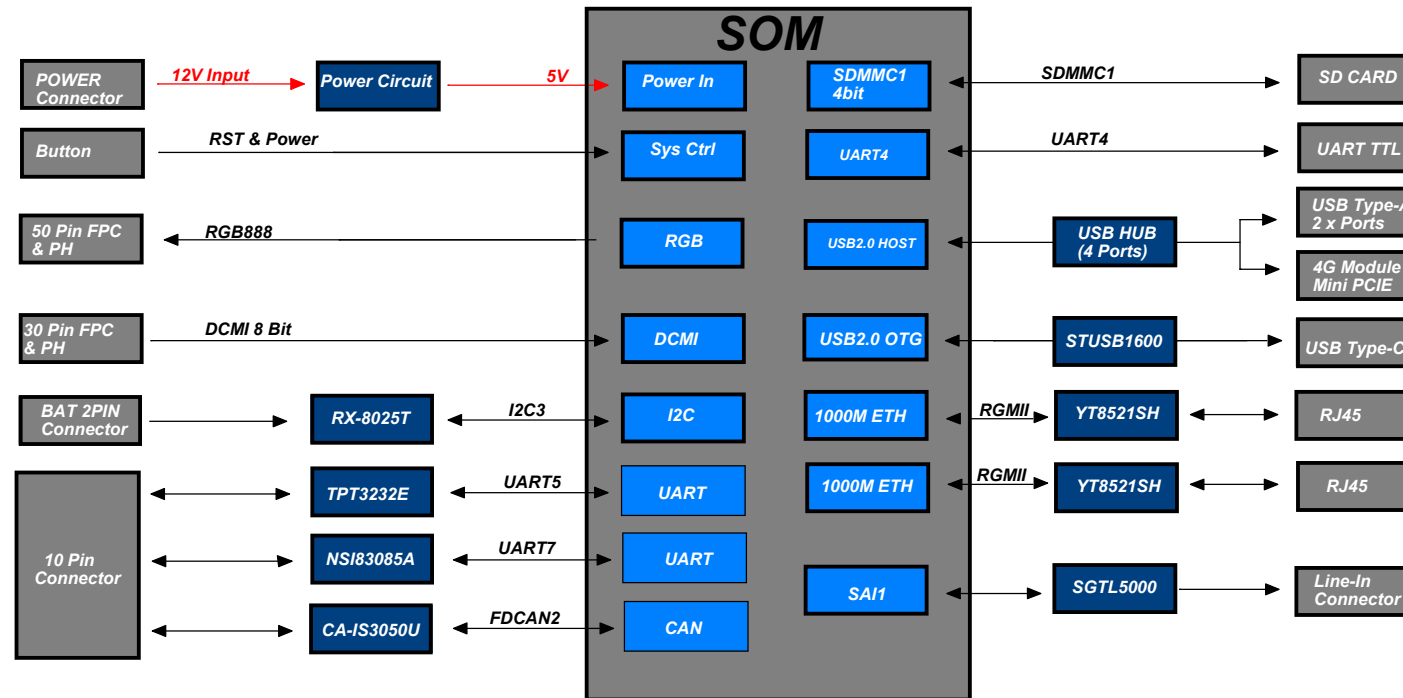
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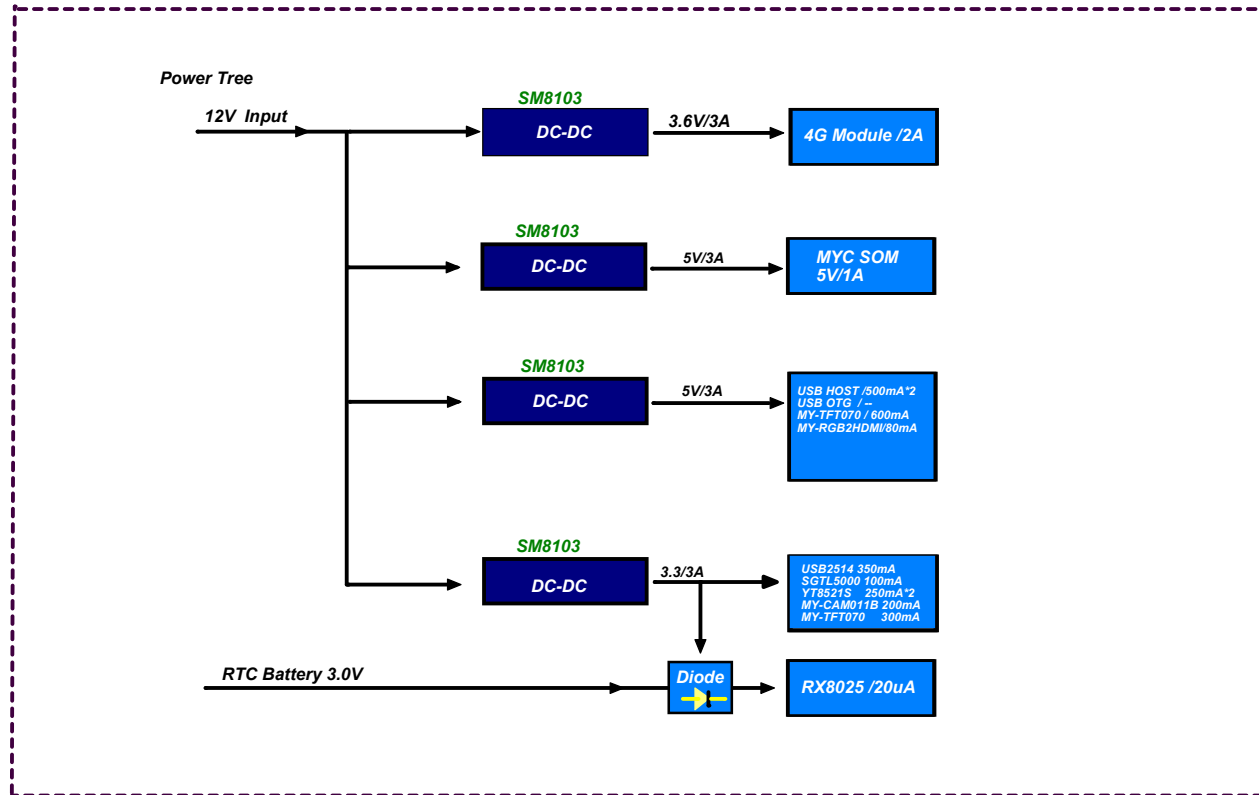
- 1. Cover
- 2. Block Diagram
- 3. Power Tree
- 4. Power Supply
- 5. RGB & DCMI
- 6. USB
- 7. SD & BOOT & LED & Debug
- 8. PH
- 9. Audio,RTC,IO
- 10. ENET x2
- 11. 4G MINI -PCIE
- 12. MYC Connect
- 13. Mechanical

VERSION HISTORY

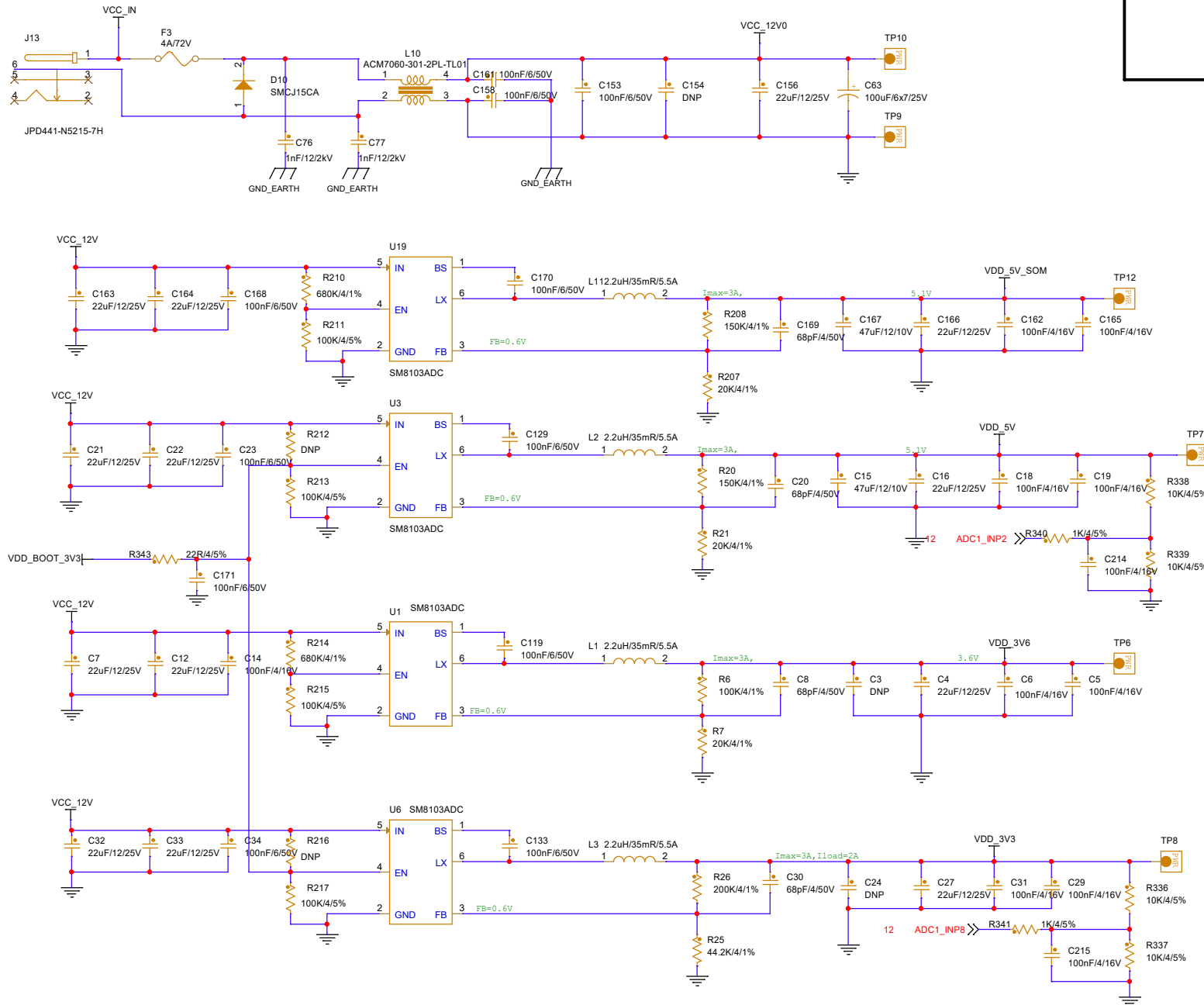
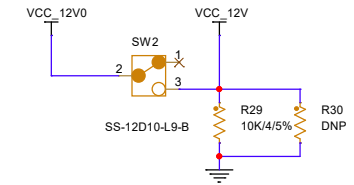
Revision	Description	Date	Drawn	Checked	Approved
V01	Initial Edition	2021-09-24			
V02		2023-02-28			
V10	Release version	2023-04-18			

Carrier Board

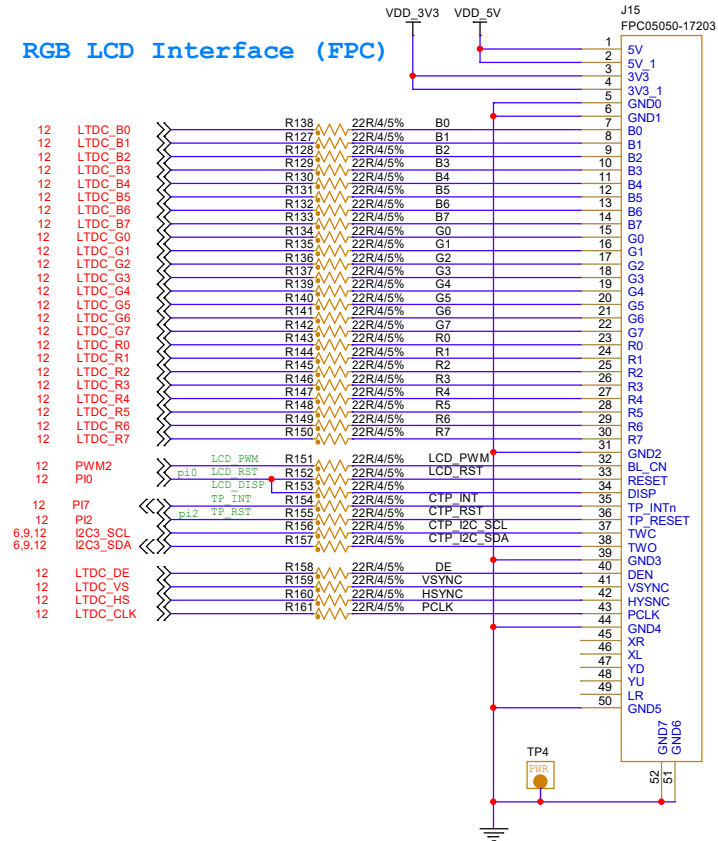




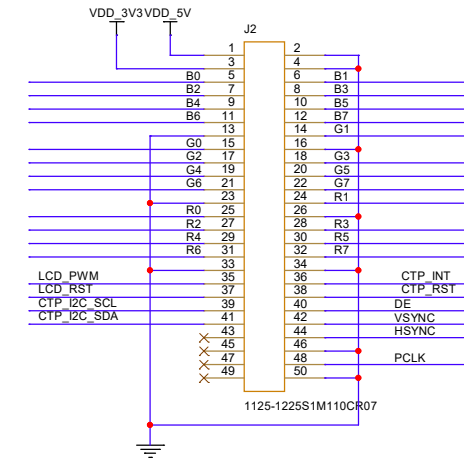
Power Switch (On/Off)



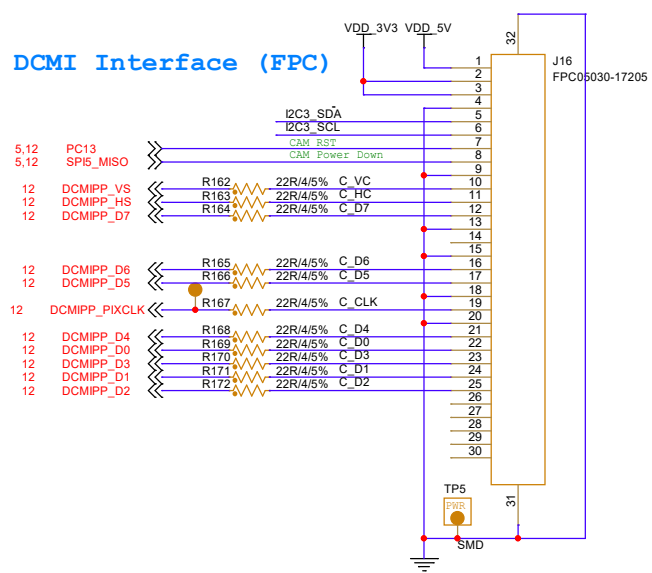
RGB LCD Interface (FPC)



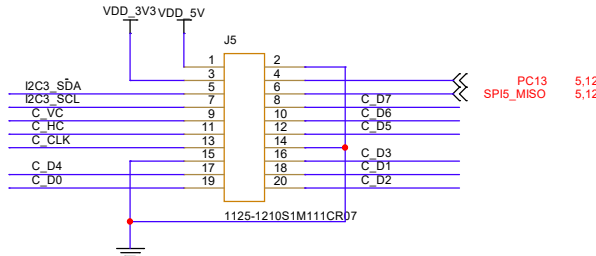
Pin Header 2x25



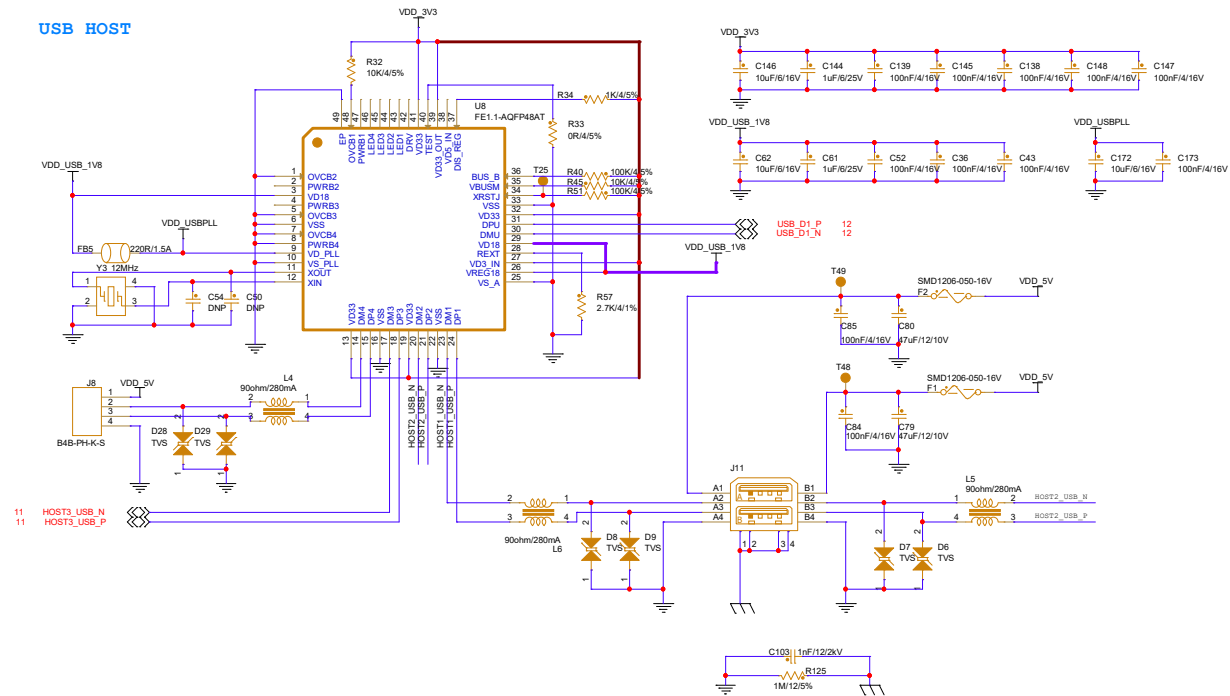
DCMI Interface (FPC)



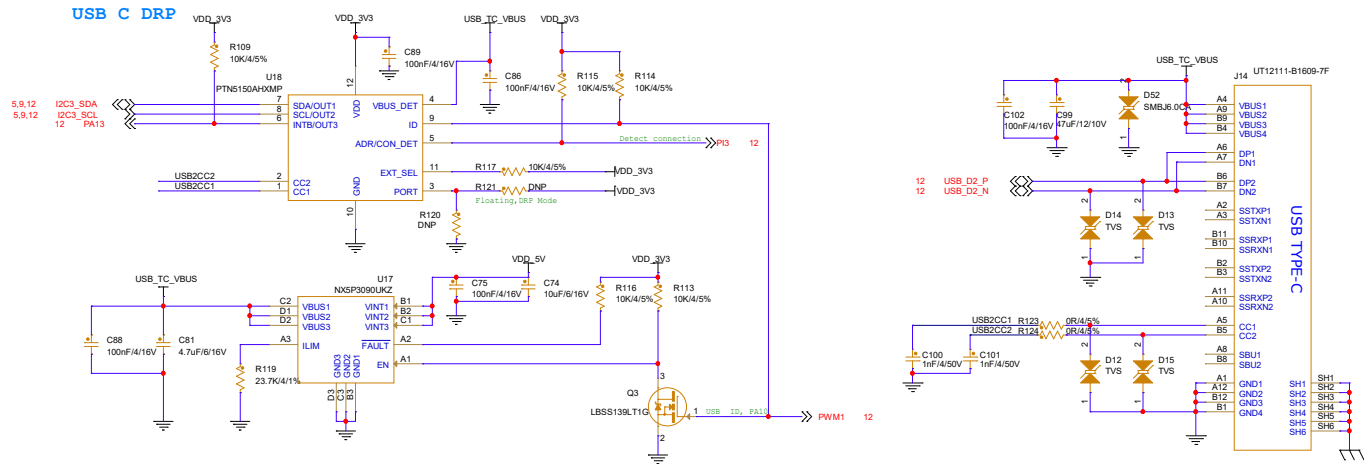
Pin Header 2x10

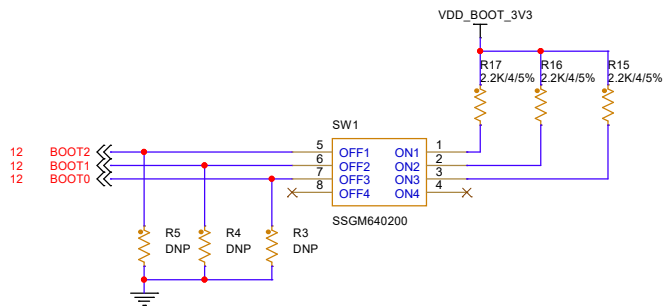
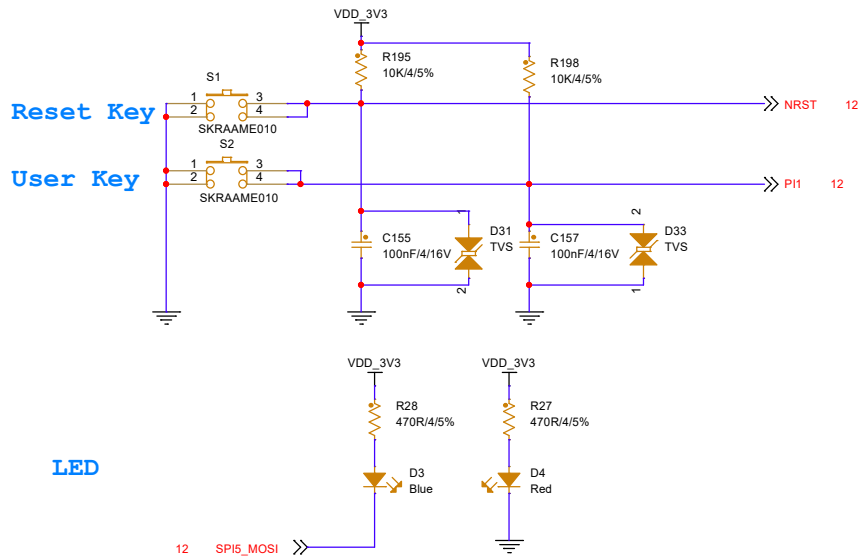


USB HOST



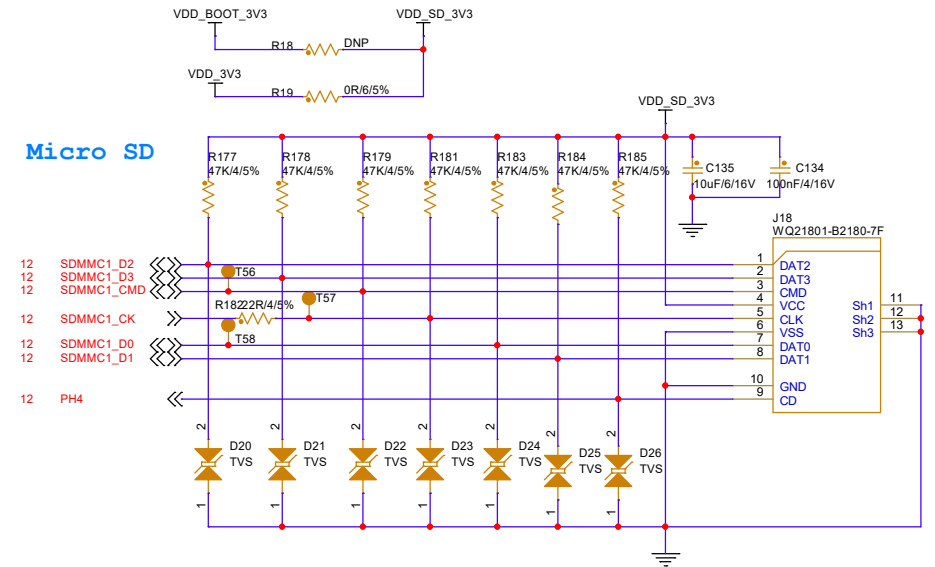
USB C DRP



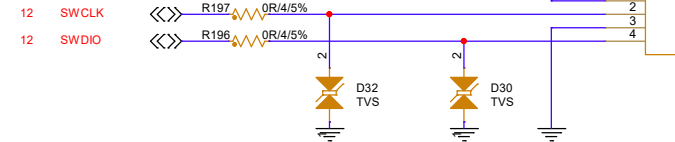


B2	B1	B0	Initial boot mode	Comments
0	0	0	UART and USB	Download image
0	1	0	eMMC	Boot from eMMC
0	1	1	Nand Flash	Boot from Nand Flash
1	0	1	SD Card	Boot from Micro SD
1	1	1	QSPI Nand Flash	BOOT from QSPI Nand Flash

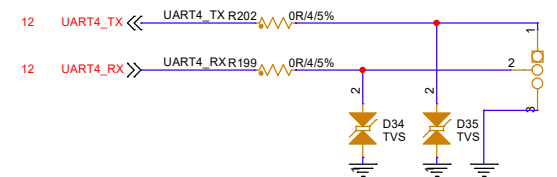
Micro SD



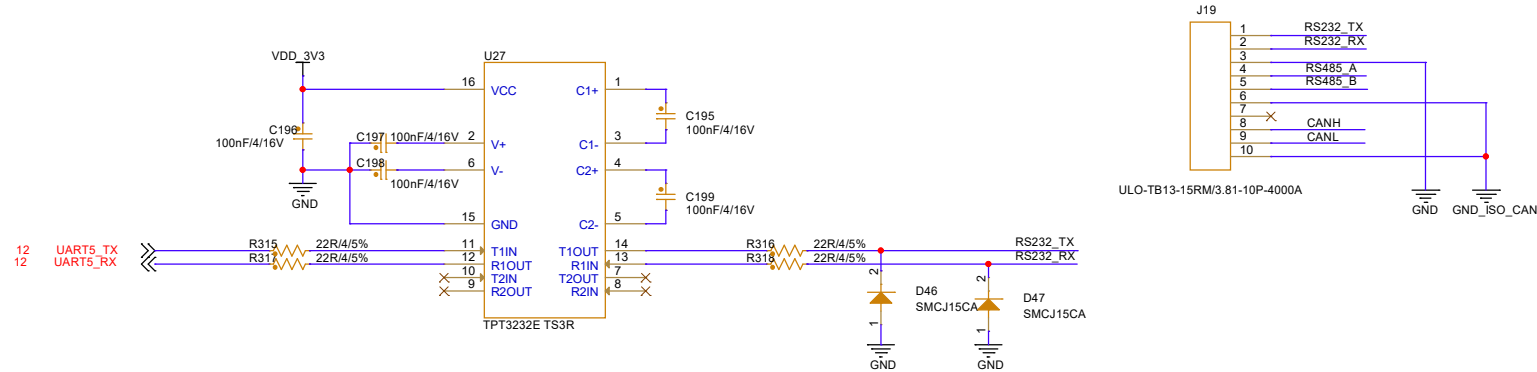
SWD Debug



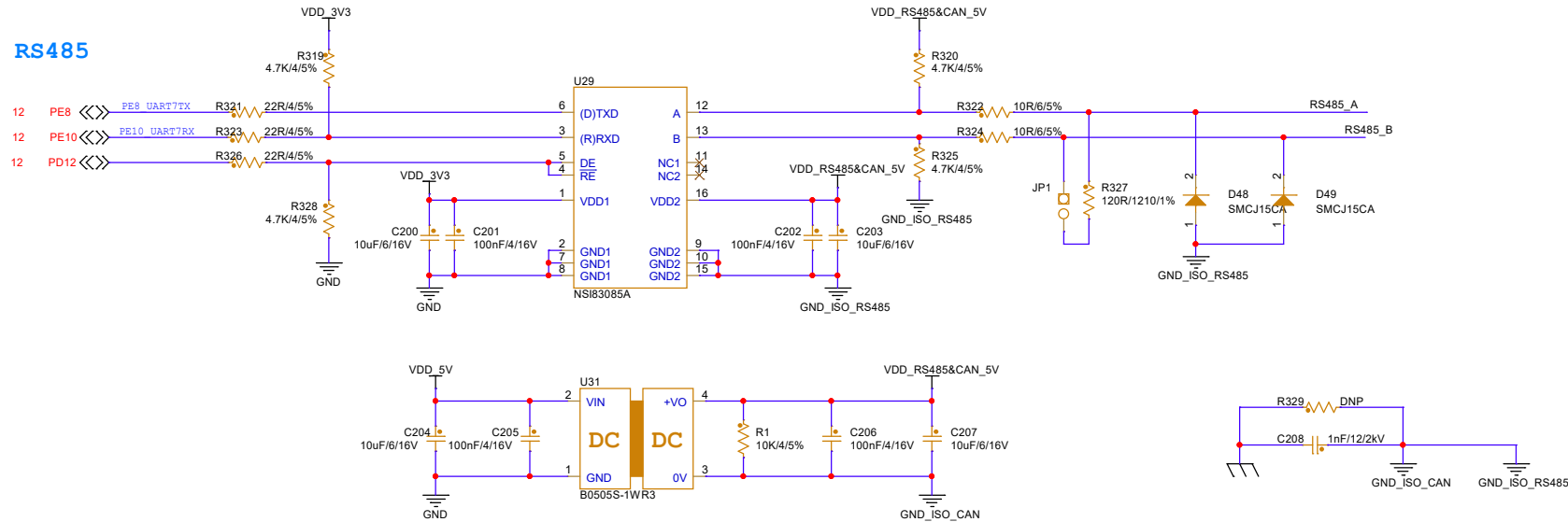
Debug Uart



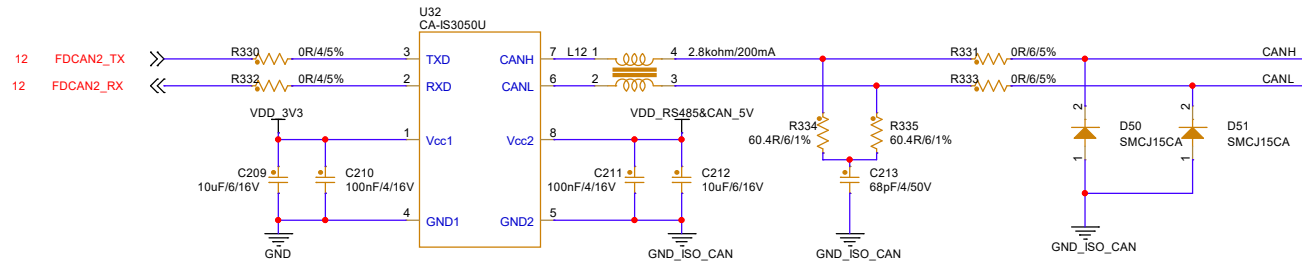
RS232

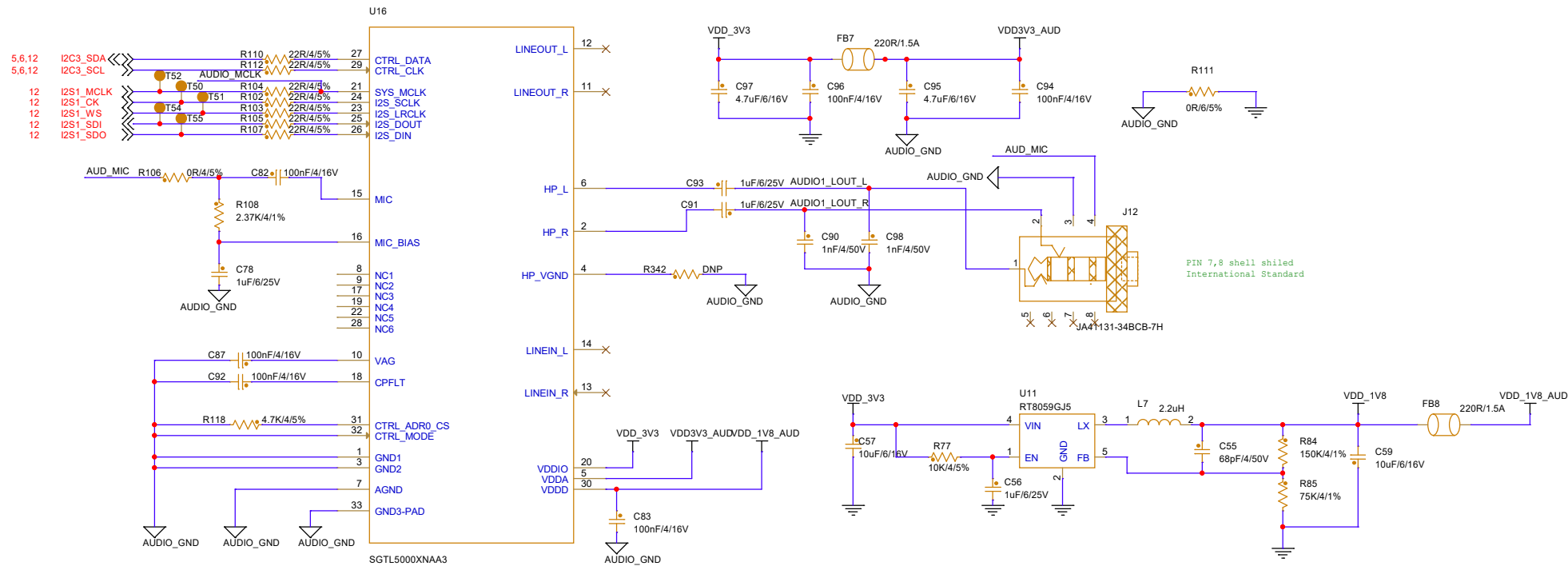


RS485



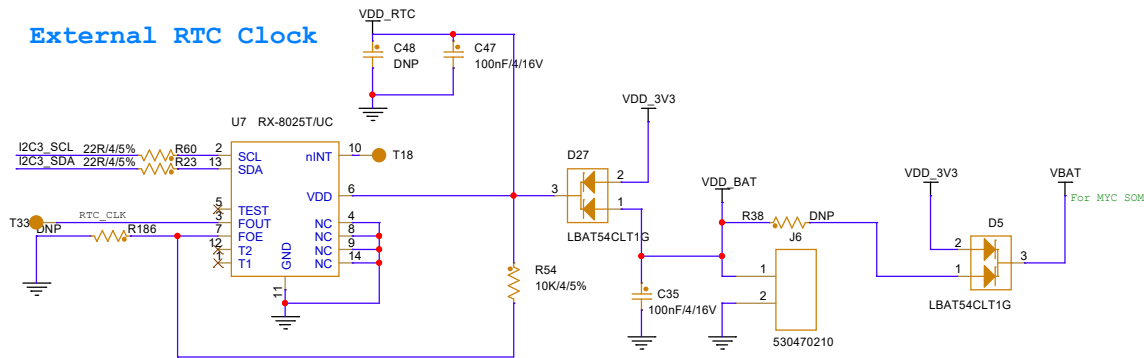
CAN

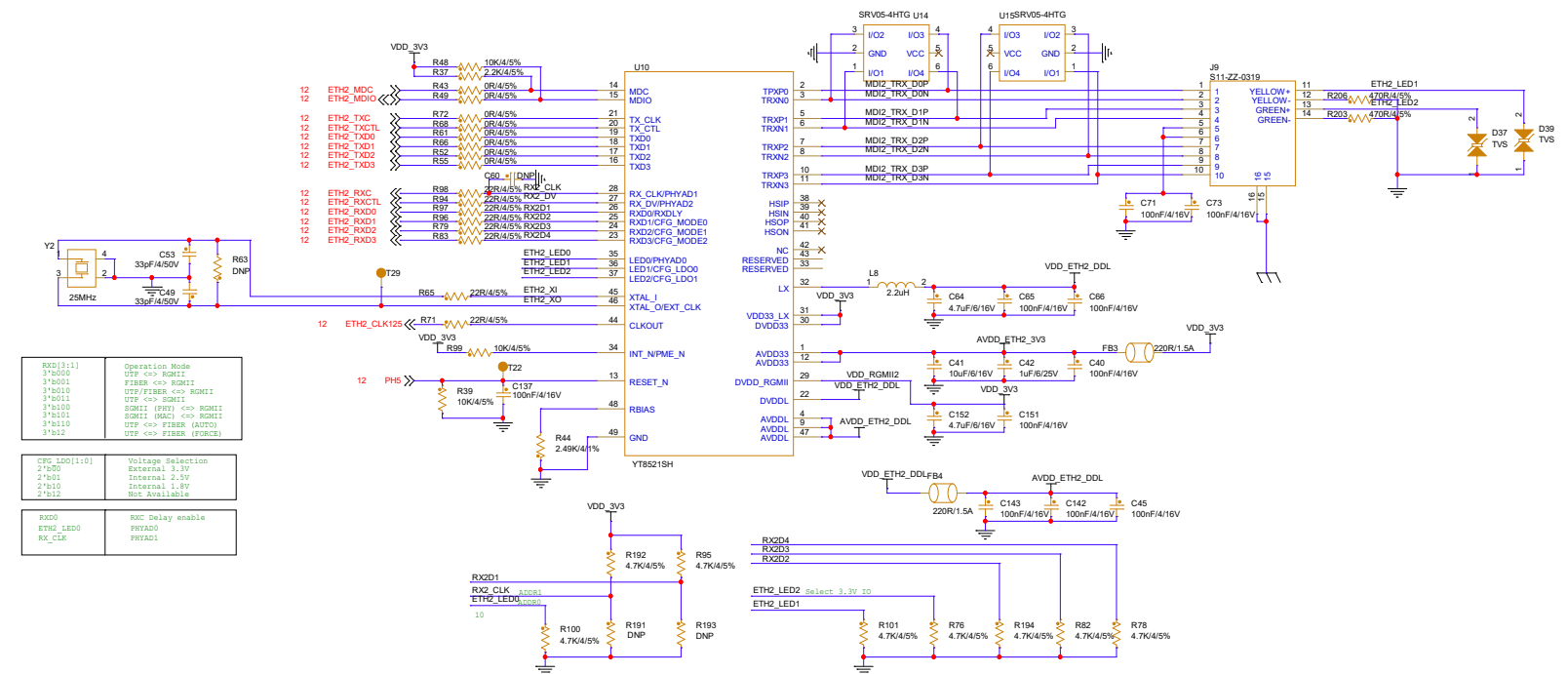
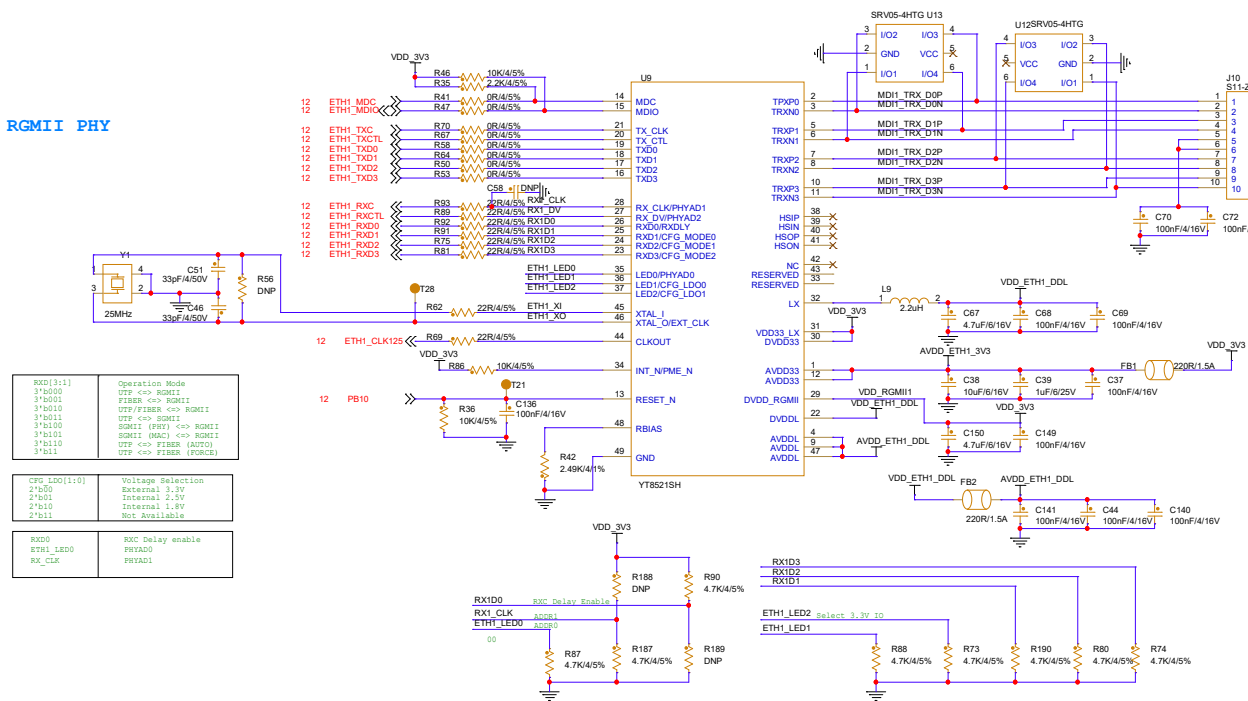


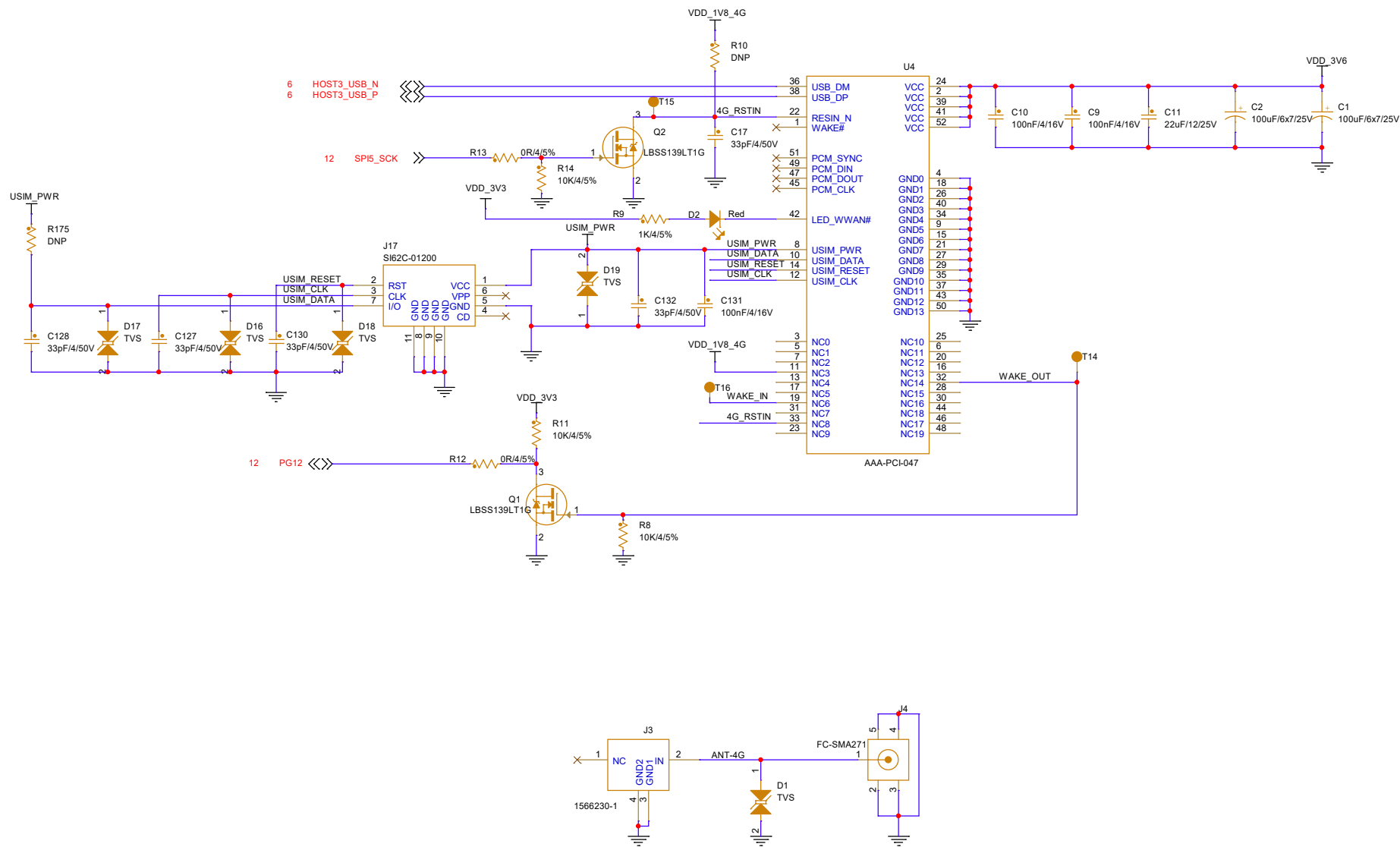


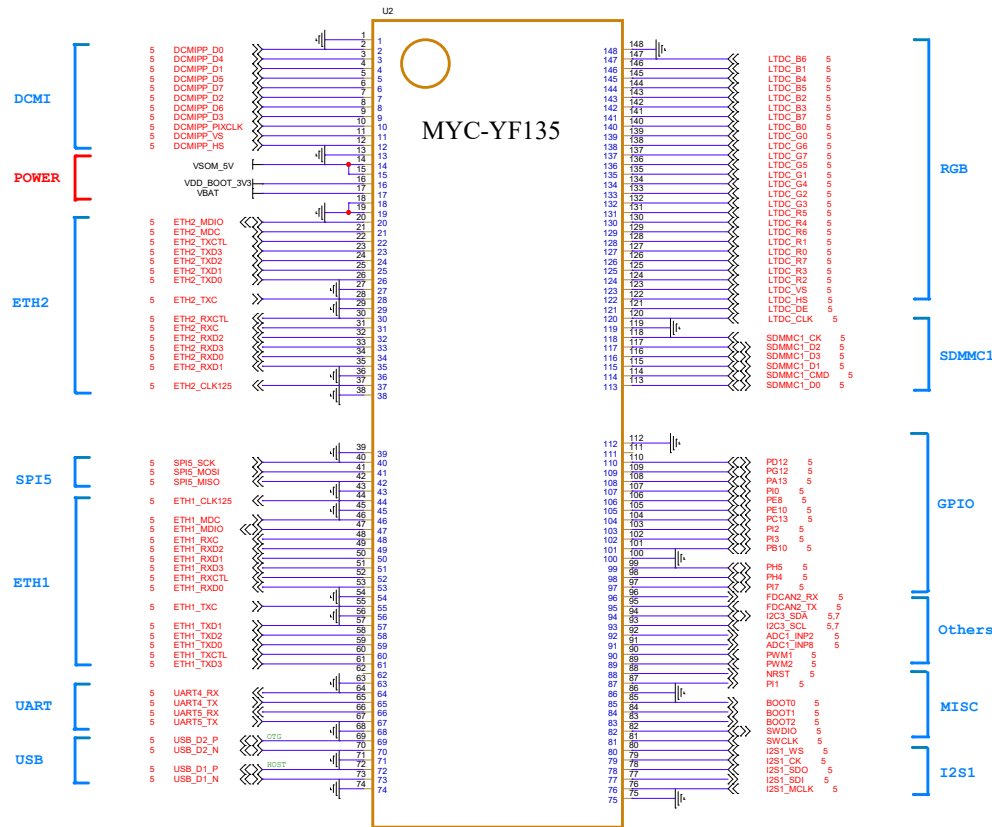
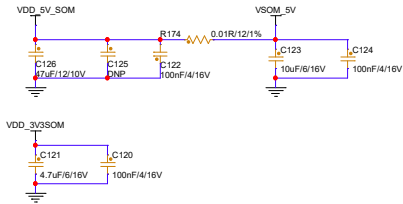
PIN 7,8 shell shielded
International Standard

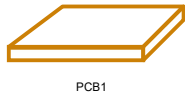
External RTC Clock



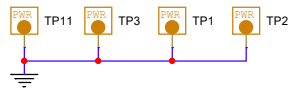








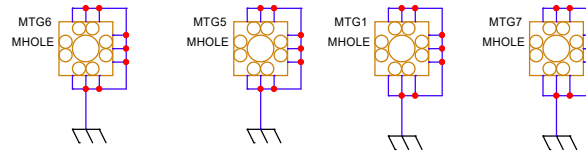
Mark Point



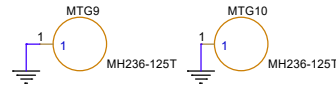
GND Test Point



MYIR PCB Template



Board Mechanical Hole



Mechanical Hole for Mounting 4G Module